# COMPUTER SCIENCE AND ENGINEERING

2020 - 2021

# SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR (AUTONOMOUS) (Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapur), (Accredited by NBA & NAAC with "A" Grade) Siddharth Nagar, Narayanavanam Road, Puttur -517583, Chittoor Dist., A.P, India Department of Computer Science and Engineering

Minutes of 6<sup>th</sup> BoS Meeting of Computer Science and Engineering (CSE)

### Date: 19/01/2021

The 6<sup>th</sup> meeting of Board of Studies (BoS) in Computer Science and Engineering is held on 19<sup>th</sup> January, 2021 (Tuesday) at 10.30 A.M in the Department of Computer Science and Engineering, Siddharth Institute of Engineering & Technology, Puttur, Chittoor –Dist. through Zoom online because of Covid Pandemic situation.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. B.Geethavani, Chairman - BoS chaired the meeting and welcomed all the members to the sixth BoS meeting and discussed about the following agenda:

- 1. To discuss and frame the course structure & syllabi for I year B.Tech. under R20 Regulation as per APSCHE suggestions.
- 2. To prepare panel of examiners and paper setters for I B.Tech. under R20.
- 3. Any other item.

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After a brief introduction of the agenda items listed above, each agenda item were taken up for discussion and the following resolutions were passed.

### Minutes:

#### Agenda: 1

Approval of I Year Course Structure and syllabi for B.Tech-CSE under R20 Regulation.

### **Resolution:** 1

After detailed discussion the BOS resolved to approve the R20 Regulation, Course Structure and syllabi for I year B.Tech (given in **Annexure –I**) applicable from the A.Y.2020-2021. After the thorough discussion, syllabus was framed to make the students acquire the required technical knowledge and skills. The BOS resolved to approve the syllabi framed for the I-year B.Tech 1 & II- Semesters (given in **Annexure-II**)

# A. Course & Syllabus Comparison

With reference to the R19 regulations, the new regulation (R20) syllabus for I year has the following modifications, which are given in the below table.

S.No	R19 Regulation	R20 Regulation	Percentage of course content changed
1	Algebra and Calculus	Algebra and Calculus	30
2	Semiconductor Physics	Applied Physics	20
3	Engineering Graphics	Engineering Graphics	0
4	Semiconductor Physics Lab	Applied Physics Lab	0
5	Workshop Practices Lab	Workshop Practice Lab	0
6	Applied Chemistry	Applied Chemistry	20
7	Communicative English	Communicative English	0
8	Probability & Statistics	Probability & Statistics	0
9	Basic Electrical & Electronics Engineering	Basic Electronics Engineering	40
10	Digital Logic Design	Digital Logic Design	0
11	Applied Chemistry Lab	Applied Chemistry Lab	10
12	Communicative English Lab	Communicative English Lab	0
13		Principles of Electrical Engineering	100
14		C Programming and Data Structures	100

#### I B.Tech

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1.7	Basic Electrical and Electronics	100
15	Engineering Lab	
16	C Programming and Data	100
16	Structures Lab	
17	Indian Constitution	100

## **Consolidated Sheet**

Course	Total courses	Percentage of syllabus changed
CSE B.Tech I Year	17	36.47

### **B. Course Relevance**

The course that comes under the category of Employability, Skill or Entrepreneurship development are shown in the table below.

S.No	Course Title	Course Code	Relevance	
1	Principles of Electrical Engineering	20EE0250	Skill Development	
2	C Programming and Data Structures	20CS0501	Employability	
3	Basic Electronics Engineering	20EC0445	Skill Development	
4	Basic Electrical and Electronics Engineering Lab	20EE0252	Skill Development	
5	C Programming and Data Structures Lab	20CS0502	Skill Development	
6	Communicative English	20HS0810	Skill Development	
7	Engineering Graphics	20ME0301	Skill Development	
8	Digital Logic Design	20CS0503	Employability	
9	Communicative English Lab	20HS0811	Skill Development	
10	Workshop Practice Lab	20ME0302	Skill Development	
11	Indian Constitution	20HS0816	Skill Development	

Modifications described above are carried out to the curriculum after discussions in the BoS by considering the feedback/suggestions from the stakeholders viz. students, alumni, faculty and employers.

### Agenda:2

Approval of panel of examiners and paper setters for I B.Tech. under R20 Regulations.

## Resolution: 2

Approved the panel of examiners and paper setters for I B.Tech. under R20 (given in Annexure–II) to be submitted to the college academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BOS. The meeting was concluded with Vote of Thanks proposed by the Chairman-BOS.

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S. No	Member Name	<b>Designation/ Organization</b>	Role of BoS	Signature
1	Dr. B. Geethavani	Professor & HOD	Chairman	B. Cut
2	Dr. P.M.S.S. Chandu	Professor	Member	B
3	Mr. R. G. Kumar	Associate Professor	Member	Frozet.
4	Mr. P. Santhosh Kumar	Associate Professor	Member	P. Sur
5	Mrs. G. Bhuvaneswari	Assistant Professor	Member	abluer.
6	Dr. S. Jyothi	Professor, Dept. of Computer Science, Sri Padmavathi Mahila Visvavidyalayam, Tirupati. Contact No. 94405 82187 Email: jyothi.spmvv@gmail.com	Member	5. Note
7	Dr. S. Ramakrishna	Professor, Department of Computer Science, SVU, Tirupati. Contact No. 9441495972, Email: <u>drsramakrishna@yahoo.com</u>	Member	S. Ro- cre,
8	Prof. P. Viswanath,	Professor, Indian Institute of Information Technology, Sri City, Chittoor Dist., Andhra Pradesh. Contact No. 7337324906, Email: <u>viswanath.p@iiits.in</u> , <u>viswanath.pulabaigari@gmail.com</u>	Member	P. Vigwowald.
9	Mr. D. Sriram Deshpande	Senior Project Engineer, Smart Enovation India Limited, Bangalore, Karnataka. Contact No. 8861345177, 9960506473 Email: <u>sriram.d@smartenovations.com</u> ; <u>sriramdeshpande@gmail.com</u>	Member	Svirom
10	Mr.C.Srikanth	Assistant Manager, Bank of Baroda, Chennai Contact No. 9177728557 Email: <u>srikanth.c87@gmail.com</u>	Member	ABSENT

**Members Present for BoS Meeting**