SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR
(AUTONOMOUS)

4ᵗʰBoS Meeting of Electronics and Communication Engineering (ECE)

Date: 14/08/2019

The 4ᵗʰmeeting of Board of Studies (BoS) in Electronics and Communication Engineering is held on 14ᵗʰAugust,2019 (Wednesday) at 2.00PM in the Department of Electronics and Communication Engineering, Siddharth Institute of Engineering & Technology, Puttur, Chittoor –Dist.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. P.Ratna Kamala, Chairman - BoS chaired the meeting and welcomed all the members to the fourth BoS meeting and discussed the following agenda:

1. Approvalof course structure for I year UG and PGin ECE w.e.f., 2019-2020.
2. Approval of syllabi for I year UG and PG in ECE w.e.f., 2019-2020.
3. Approval of Panel of Question Paper setters.
4. Approval of Panel of Examiners.
5. Any other item with the permission of Chair.

After a brief introduction of the agenda items listed above, each agenda item were taken up for discussion and the following resolutions were passed.

Minutes:

Agenda: 1

Approval of course structure for I year UG and PG in ECE w.e.f.,2019-2020.

Resolution: 1

After detailed discussion, the BOS resolved to approve the course structure for PG and I year UG(given in **Annexure –I**) applicable from the A.Y.2019-2020.

Agenda: 2

Approval of syllabus for I year UG and PG in ECE w.e.f., 2019-2020.

Resolution: 2

After thorough discussion, syllabus was framed to make the students acquire with the required technical knowledge and skills. The BOS resolved to approve the syllabi framed for the I Year B.Tech I & II Semesters and PG

(given in **Annexure-II**).

A. Course & Syllabus Comparison

With reference to the R18 regulations, the new regulation (R19) syllabus for I[st] year has the following modifications, which are given in the below table.

I B.Tech

S.No	R18 Regulation	R19 Regulation	Percentage of course content changed
1.	Mathematics-I	Algebra and Calculus	60
2.	Chemistry	Applied Chemistry	40
3.	Engineering Graphics	Engineering Graphics& Design	20
4.	Chemistry lab	Applied Chemistry lab	0
5.	Workshop Practice Lab	Workshop Practice Lab	0
6.		Python Programming Lab	100

7.		Communicative English	100
8.	Mathematics-II	Differential Equations and Vector Calculus	60
9.	Semiconductor Physics	Semiconductor Physics	0
10.	Basic Electrical Engineering	Basic Electrical Engineering	0
11.		Switching Theory and Logic Design	100
12.		Communicative English Lab	100
13.		Semiconductor Physics Lab	100
14.		Essence of Indian Traditional Knowledge	100
15.	Python Programming	Python Programming	0

Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
ECE B.Tech I Year	15	52

I M.Tech

S.No	R18 Regulation	R19 Regulation	Percentage of course content changed
1.	VLSI Technology	VLSI Technology	0
2.	Digital IC Design	Digital IC Design	0
3.	ASIC Design	ASIC Design	0
4.	System Modeling & Simulation	System Modeling & Simulation	0
5.	Embedded System Design	Embedded System Design	0
6.	Verilog HDL	Verilog HDL	0
7.	Analog IC Design	Analog IC Design	0
8.	Image & Video Processing	Image & Video Processing	0

9.	Digital Electronic Circuits Lab (Virtual Lab)	Digital Electronic Circuits Lab (Virtual Lab)	0
10.	Digital IC Design Lab	Digital IC Design Lab	0
11.	Research Methodology and IPR	Research Methodology and IPR	0
12.	English for Research Paper Writing	English for Research Paper Writing	0
13.	FPGA Architectures & Applications	FPGA Architectures & Applications	0
14.	Low Power VLSI Design	Low Power VLSI Design	0
15.	Nano Electronics	Nano Electronics	0
16.	Algorithms for VLSI Design Automation	Algorithms for VLSI Design Automation	0
17.	Advanced Digital System Design	Advanced Digital System Design	0
18.	Testing & Testability	Testing & Testability	0
19.	Real Time Operating System	Real Time Operating System	0
20.	Solid State Device Modeling and Simulation	Solid State Device Modeling and Simulation	0
21.	Mixed Signal Lab	Mixed Signal Lab	0
22.	Digital VLSI Design Lab (Virtual Lab)	Digital VLSI Design Lab (Virtual Lab)	0
23.	Mini Project	Mini Project	0
24.	Constitution of India	Constitution of India	0
25.	Advanced Digital System Design	Advanced Digital System Design	0
26.	Advanced Digital Signal Processing	Advanced Digital Signal Processing	0
27.	Antenna and Radiating Systems	Antenna and Radiating Systems	0
28.	Digital Communication Techniques	Digital Communication Techniques	100
29.	DSP Processors & Architectures	DSP Processors & Architectures	0
30.	High Speed Networks	High Speed Networks	100
31.	Voice and Data Networks	Voice and Data Networks	100
32.	Wireless Sensor Networks	Wireless Sensor Networks	100
33.	Advanced Digital Signal Processing Lab(Virtual Lab)	Advanced Digital Signal Processing Lab(Virtual Lab)	20
34.	Advanced Digital System Design Lab	Advanced Digital System Design Lab	0
35.	Research Methodology and IPR	Research Methodology and IPR	0

No.	Subject	Subject	Score
36.	English for Research Paper Writing	English for Research Paper Writing	0
37.	Wireless Communications	Disaster Management	100
38.	Coding Theory & Techniques	Sanskrit for Technical Knowledge	100
39.	Introduction to IoT	Value Education	100
40.	Adaptive Signal Processing	Wireless Communications	0
41.	Cognitive Radio	Coding Theory & Techniques	0
42.	Image & Video Processing	Introduction to IoT	0
43.	Pattern Recognition and Machine learning	Adaptive Signal Processing	0
44.	Detection & Estimation of Signals	Cognitive Radio	0
45.	Advanced Communications Lab (Virtual Lab)	Image & Video Processing	0
46.	Image & Video Processing Lab	Pattern Recognition and Machine learning	0
47.	Mini Project	Detection & Estimation of Signals	0
48.	Constitution of India	Advanced Communications Lab	0
49.	Wireless Communications	Image & Video Processing Lab	0
50.		Mini Project	0
51.		Constitution of India	100
52.		Pedagogy Studies	100
53.		Stress Management by Yoga	100
54.		Personality Development through Life Enlightenment Skills.	100
55.	Embedded System Design	Embedded System Design	0
56.	Sensors and Actuators	Sensors and Actuators	0
57.	Structural Digital System Design	Structural Digital System Design	0
58.	FPGA Architectures & Applications	FPGA Architectures & Applications	0
59.	Real Time Operating Systems	Real Time Operating Systems	0
60.	Embedded Networking	Embedded Networking	0
61.	Wireless Communications	Wireless Communications	0
62.	Internet Protocols	Internet Protocols	0
63.	Embedded System Design Lab	Embedded System Design Lab	0

64.	Structural Digital System Design Lab	Structural Digital System Design Lab	0
65.	Research Methodology and IPR	Research Methodology and IPR	0
66.	English for Research Paper Writing	English for Research Paper Writing	
67.		Research Methodology and IPR	0
68.	Introduction to IoT	Introduction to IoT	100
69.	Advanced Microcontrollers	Advanced Microcontrollers	0
70.	Hardware Software Co-Design	Hardware Software Co-Design	0
71.	Testing & Testability	Testing & Testability	0
72.	Micro Electromechanical Systems	Micro Electromechanical Systems	0
73.	VLSI Technology	VLSI Technology	0
74.	Digital IC Design	Digital IC Design	10
75.	Wireless Sensor Networks	Wireless Sensor Networks	0
76.	Internet of Things Lab	Internet of Things Lab	0
77.	Microcontrollers & Interfacing Lab	Microcontrollers & Interfacing Lab	0
78.	Mini Project	Mini Project	0
79.	Constitution of India	Constitution of India	0
80.		Digital IC Design	80
			100

Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
ECEM.TechI Year	80	17.62

B. Course Relevance

The courses that come under the category of Employability, Skill or Entrepreneurship Development are shown in the table below.

I B.Tech

S.No	Course Title	Course Code	Relevance
1.	Switching Theory and Logic Design	19EC0401	Employability
2.	Python Programming	19CS0501	Skill Development
3.	Python Programming Lab	19CS0502	Skill Development
4.	Communicative English	19HS0810	Skill Development
5.	Communicative English Lab	19HS0811	Skill Development

I M.Tech

S.No	Course Title	Course Code	Relevance
1.	Research Methodology and IPR	19HS0823	Skill Development
2.	VLSI Technology	19EC4201	Employability
3.	Digital IC Design	19EC4202	Employability
4.	ASIC Design	19EC4203	Employability
5.	System Modeling & Simulation	19EC4204	Employability
6.	Embedded System Design	19EC4101	Employability
7.	Verilog HDL	19EC4205	Employability
8.	Analog IC Design	19EC4206	Employability
9.	Image & Video Processing	19EC4015	Employability
10.	Digital Electronic Circuits Lab (Virtual Lab)	19EC4207	Skill Development
11.	Digital IC Design Lab	19EC4208	Skill Development

12.	English for Research Paper Writing	19HS0818	Skill Development
13.	FPGA Architectures & Applications	19EC4209	Employability
14.	Low Power VLSI Design	19EC4210	Employability
15.	Nano Electronics	19EC4211	Employability
16.	Algorithms for VLSI Design Automation	19EC4212	Employability
17.	Advanced Digital System Design	19EC4001	Employability
18.	Testing & Testability	19EC4213	Employability
19.	Real Time Operating System	19EC4104	Employability
20.	Solid State Device Modeling and Simulation	19EC4214	Employability
21.	Mixed Signal Lab	19EC4215	Skill Development
22.	Digital VLSI Design Lab (Virtual Lab)	19EC4216	Skill Development
23.	Constitution of India	19HS0829	Skill Development
24.	Research Methodology and IPR	19HS0823	Skill Development
25.	Advanced Digital System Design	19EC4001	Employability
26.	Advanced Digital Signal Processing	19EC4002	Employability
27.	Antenna and Radiating Systems	19EC4103	Employability
28.	Digital Communication Techniques	19EC4004	Employability
29.	DSP Processors & Architectures	19EC4005	Employability
30.	High Speed Networks	19EC4006	Employability
31.	Voice and Data Networks	19EC4007	Employability
32.	Wireless Sensor Networks	19EC4008	Employability
33.	Advanced Digital Signal Processing Lab(Virtual Lab)	19EC4009	Skill Development
34.	Advanced Digital System Design Lab	19EC4010	Skill Development

35.	English for Research Paper Writing	19HS0818	Skill Development
36.	Wireless Communications	19EC4011	Skill Development
37.	Coding Theory & Techniques	19EC4012	Employability
38.	Introduction to IoT	19EC4109	Skill Development
39.	Adaptive Signal Processing	19EC4013	Employability
40.	Cognitive Radio	19EC4014	Employability
41.	Image & Video Processing	19EC4015	Employability
42.	Pattern Recognition and Machine learning	19EC4016	Employability
43.	Detection & Estimation of Signals	19EC4017	Employability
44.	Advanced Communications Lab	19EC4018	Skill Development
45.	Image & Video Processing Lab	19EC4019	Skill Development
46.	Mini Project	19EC4020	Skill Development
47.	Constitution of India	19HS0829	Skill Development
48.	Research Methodology and IPR	19HS0823	Skill Development
49.	Embedded System Design	19EC4101	Employability
50.	Sensors and Actuators	19EC4102	Employability
51.	Structural Digital System Design	19EC4103	Employability
52.	FPGA Architectures & Applications	19EC4209	Employability
53.	Real Time Operating Systems	19EC4104	Employability
54.	Embedded Networking	19EC4105	Employability
55.	Wireless Communications	19EC4011	Employability
56.	Internet Protocols	19EC4106	Employability
57.	Embedded System Design Lab	19EC4107	Skill Development
58.	Structural Digital System Design Lab	19EC4108	Skill Development

59.	English for Research Paper Writing	19HS0818	Skill Development
60.	Introduction to IoT	19EC4109	Skill Development
61.	Advanced Microcontrollers	19EC4110	Employability
62.	Hardware Software Co-Design	19EC4111	Employability
63.	Testing & Testability	19EC4213	Employability
64.	Micro Electromechanical Systems	19EC4112	Skill Development
65.	VLSI Technology	19EC4201	Employability
66.	Digital IC Design	19EC4202	Employability
67.	Wireless Sensor Networks	19EC4008	Employability
68.	Internet of Things Lab	19EC4113	Skill Development
69.	Microcontrollers & Interfacing Lab	19EC4114	Skill Development
70.	Mini Project	19EC4115	Employability
71.	Constitution of India	19HS0829	Skill Development

Modifications described above are carried out to the curriculum after discussions in the BoS by considering the feedback/suggestions from the stake holders viz. students, alumni, faculty and employers.

Agenda: 3

Approval of Panel of question paper setters

Resolution: 3

Approved the panel of question paper setting (given in **Annexure –IV**) to be submitted to the college Academic council for approval.

Agenda: 4

Approval of Panel of examiners

Resolution:4

Approved the panel of examiners prepared for valuation (given in **Annexure –V**) to be submitted to the college Academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BOS. The meeting was concluded with vote of thanks proposed by the Chairman-BOS.

Members Present

S. No.	Member Name	Designation/Organisation	Role of BOS	Signature
1.	Dr. P. Ratna Kamala	Professor & HOD	Chairman	
2.	Dr. P.G.Kuppaswamy	Professor (Signal Processing)	Member	
3.	N. Vamsi Praveen	Associate Professor (VLSI System Design)	Member	
4.	J. Rajanikanth	Associate Professor (DECS)	Member	
5.	S. Sundara Babu	Associate Professor (Embedded Systems)	Member	
6.	Dr T. Ramashri	Professor Sri Venkateswara University College of Engineering, Tirupati	Member	
7.	Dr. K.P. Naveen	Assistant Professor, IIT Tirupathi	Member	
8.	Dr R.V.S. Satyanarayana	Professor, S.V. University, Tirupati	Member	
9.	Mr. M. Lakshmi Narayana	Junior Telecom Officer, BSNL OFC Transmission maintenance STR Wing, Kolar Karnataka	Member	
10.	Mr V. Prasanth	Software Developer Dxc Technologies Chennai.	Member	