SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR (AUTONOMOUS)

5th BoS Meeting of Electronics and Communication Engineering (ECE)

Date: 28/08/2020

The 5th meeting of Board of Studies (BoS) in Electronics and Communication Engineering is held on 28thAugust,2020 (Friday) at 10.00AM online through ZOOM.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

- Dr. P. Ratna Kamala, Chairman BoS chaired the meeting and welcomed all the members to the fifth BoS meeting and discussed the following agenda:
- 1. Approval of course structure and syllabi for I year B.Tech. under R20 Regulation in ECE w.e.f., 2020-2021.
- 2. Approval of course structure and syllabi for II year B.Tech. under R19 Regulation.
- Approval of course structure and syllabi for III year B.Tech. under R18 Regulation.
- Approval of course structure and syllabi for I & II years M.Tech. under R20 Regulation and II year
 M. Tech under R19 regulation.
- Approval of examiners and paper setters for II and III B.Tech. that comes under R20, R19 & R18 respectively.
- 6. Approval of panel of examiners and paper setters for I & II M.Tech. that comes under R20 regulation and II M.Tech. under R19 regulation.
- 7. Any other item with the permission of Chair.
 - i. Formulated/Modified Vision, Mission, PEO's and PSO's

After a brief introduction of the agenda items listed above, each agenda item were taken up for discussion and the following resolutions were passed.

Minutes:

Agenda: 1

Approval of course structure and syllabi for I year B.Tech. under R20 Regulation in ECE w.e.f., 2020-2021.

Resolution: 1

After detailed discussion, the BOS resolved to approve the course structure and syllabil for I year B.Tech. under R20 Regulation (given in **Annexure –I and II** respectively) applicable from the A.Y.2020-2021.

Agenda: 2

Approval of course structure and syllabi for II year B.Tech. under R19 Regulation in ECE w.e.f., 2020-2021.

Resolution: 2

After detailed discussion, the BOS resolved to approve the course structure for 11 year B.Tech. under R19 Regulation (given in Annexure –I and II respectively) with minor suggestions applicable from the A.Y.2020-2021.

A. Course & Syllabus Comparison

With reference to the R16 regulations, the new regulation (R18) syllabus for 3rd year has the following modifications, which are given in the below table.

III B.Tech

S.No	R16 Regulation	R18 Regulation	Percentage of course content changed
1.	Analog Communications	Analog Communications	Ð
2.	Electronic Measurements and Instrumentation	Electronic Measurements and Instrumentation	0
3.	Linear IC Applications		0

4.	Antennas & Wave	Antennas and Wave	
	Propagation	Propagation	0
5.	Linear Control Systems	Control Systems	0
6.	Managerial Economics &	Managerial Economics and	
	Financial Analysis	Financial Analysis	0
7.		Biology for Engineers	0
8.	Analog Communications Lab	Analog Communications Lab	0
9.	Linear IC Applications Lab		0
10.		Environmental Sciences	0
11.	Comprehensive Online Examination-III		0
12.	Aptitude Practice-I		0
13.	Digital Communications	Digital Communications	0
14.	Digital Signal Processing	Digital Signal Processing	100
15.	Microprocessors &	Microprocessors and	
	Microcontrollers	Microcontrollers	60
16.	Digital IC Applications		0
17.		Data Communication and Networking	100
18.	Microwave Engineering	Microwave Theory and Techniques	45
19.		Scientific Computing	100
20.		Industrial Instrumentation	100
21.		Python Programming	100
22.	and the second of	Information Theory and Coding	100

23.	Advanced English Language and Communication Skills Lab	English for Corporate Communication Skills Lab	0
24.	Digital Communications Lab	Digital Communications Lab	0
25.	Digital IC Applications Lab	Antennas and Wave Propagation Lab (Virtual Lab)	0
26.		Microcontroller and Applications Lab	100
27.		Digital Signal Processing Lab	100
28.		Electronic Measurements Lab	100
29.		Internship (60 Hours)	100

With reference to the R18 regulations, the new regulation (R19) syllabus for 2nd year has the following modifications which are given in the below table.

II B.Tech

S.No	R18 Regulation	R19 Regulation	Percentage of course content changed
1.	Mathematics-III	Numerical Methods and	
		Transforms	20
2.	Electronic Devices	Electronic Devices and Circuits	40
3.	Digital System Design		0
4.	Signals & Systems	Signals, Systems and Random	
		Processes	35
5.	Network Theory	Network Theory	0
6.		Water Technology	:00

7.		Generation of Energy through	100
		waste	
8.		Fundamentals of Mechanical	***
		Engineering	100
9.		Linux Programming	100
10.	Electronic Devices Lab	Electronic Devices and Circuits	
		Lab	0
11.	Digital System Design Lab	Switching Theory and Logic	
		Design Lab	15
12.	Signals and Systems		
	Simulation Lab	Basic Simulation Lab	0
13.	Indian Constitution	Indian Constitution	0
14.		Linear & Digital IC Applications	100
15.	Analog Circuits	Electronic Circuit Analysis	0
16.	Analog Communications	Analog Communications	80
17.		Fundamentals of Urban Planning	0
18.		Mechanical Measurements &	
		Control Systems	0
19.		Java Programming	0
20.	Probability Theory and		
	Stochastic Processes		0
21.	Managerial Economics and	Managerial Economics and	
	Financial Analysis	Financial Analysis	0
22.		Electronic Circuit Analysis Lab	100
23.	Analog Circuits Lab		0
24.	Analog Communications Lab	Analog Communications Lab	0
25.		Linear & Digital IC Applications	
7		Lab	100
26.	Environmental Sciences	Environmental Science	0

Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
ECE B.Tech II& III Year	55	36.27

With reference to the R18 regulations, the new regulation (R19) syllabus for 2^{nd} year has the following modifications which are given in the below table.

II M.Tech

S.No	R18 Regulation	R19 Regulation	Percentage of course content changed
1.	Scripting Language for VLSI Design Automation	Scripting Language for VLSI Design Automation	0
2.	Nano Materials and Nanotechnology	Nano Materials and Nanotechnology	0
3.	Wireless Sensor Networks	Wireless Sensor Networks	0
4.	Business Analytics	Business Analytics	100
5.	Industrial Safety	Industrial Safety	0
6.	Advanced Operations Research	Advances in Operations Research	100
7.	Cost Management of Engineering Projects	Cost Management of Engineering Projects	100
8.	Composite Materials	Composite Materials	100
9.	Waste to Energy	Waste to Energy	20
10.	Dissertation Phase I	Dissertation Phase- I	0
11.	Dissertation Phase II	Dissertation Phase- II	0
12.	Optical Networks	Optical Networks	0

13.	Testing & Testability	Testing & Testability	0
14.	RF and Microwave Circuit Design	RF and Microwave Circuit Design	100
15.	Business Analytics	Business Analytics	0
16.	Industrial Safety	Industrial Safety	100
17.	Advanced Operations Research	Advances in Operations Research	100
18.	Cost Management of Engineering Projects	Cost Management of Engineering Projects	100
19.	Composite Materials	Composite Materials	20
20.	Waste to Energy	Waste to Energy	0
21.	Dissertation Phase I	Dissertation Phase- I	0
22.	Dissertation Phase II	Dissertation Phase- II	0
23.	Pedagogy Studies		0
24.	Stress Management by Yoga		0
25.		Digital IC Design	100
26.		Disaster Management	100
27.		Sanskrit for Technical Knowledge	100
28.		Value Education	100
29.	Personality Development Through Life Enlightenment Skills.		0
30.	Advanced Digital Signal Processing	Advanced Digital Signal Processing	0
31.	Radio Frequency Identification	Radio Frequency Identification	0
32.	System on Chip Architecture	System on Chip Architecture	0
33.	Business Analytics	Business Analytics	100

34.	Industrial Safety	Industrial Safety	0
35.	Advanced Operations Research	Advances in Operations Research	100
36.	Cost Management of Engineering Projects	Cost Management of Engineering Projects	100
37.	Composite Materials	Composite Materials	100
38.	Waste to Energy	Waste to Energy	20
39.	Dissertation-I	Dissertation Phase-I	0
40.	Dissertation -II	Dissertation Phase -II	0

Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
ECE M.Tech II Year	40	41.05

B. Course Relevance

The courses that come under the category of Employability, Skill or Entrepreneurship Development are shown in the table below.

П& III В.Tесh

S.No	Course Title	Course Code	Relevance
1	Analog Communications	18EC0408	Employability
	Electronic Measurements and	18EC0413	Employability
2.	Instrumentation	10 man 10	
3.	Antennas and Wave Propagation	18EC0419	Employability
4.	Control Systems	18EE0211	Employability
5.	Managerial Economics and Financial Analysis	18HS0812	Employability
6.	Biology for Engineers	18HS0803	Employability

7.	Analog Communications Lab	18EC0411	Employability
	Environmental Sciences	18HS0804	Employability
8.	Digital Communications	18EC0415	Employability
9.	Digital Signal Processing	18EC0414	Employability
10.	Microprocessors and Microcontrollers	18EC0420	Employability
11.	Data Communication and Networking	18EC0451	Employability
12.	Microwave Theory and Techniques	18EC0428	Employability
13.	Scientific Computing	18EC0430	Employability
14.	Industrial Instrumentation	18EE0234	Employability
15.	Python Programming	18CS0517	Skill Development
16.	Information Theory and Coding	18EC0429	Employability
17.	English for Corporate Communication Skills	1811S0859	Employability
18.	Lab		e Luia.
19.	Digital Communications Lab	18EC0418	Employability
	Antennas and Wave Propagation Lab (Virtual Lab)	18EC0421	Employability
20.	Microcontroller and Applications Lab	18EC0422	Employability
21.	Digital Signal Processing Lab	18EC0417	Employability
22.	Electronic Measurements Lab	18EC0416	Employability
23.	Internship (60 Hours)	18EC0425	Employability
24.	Numerical Methods and Transforms	19HS0834	Employability
25,	Electronic Devices and Circuits	19EC0402	Employability
26.	Signals, Systems and Random Processes	19EC0403	Employability
27.	Network Theory	19EE0242	Employability
28.	Water Technology	19CE0136	Skill Development
29.	Water 1997		

30.	Generation of Energy through Waste	19EE0238	Skill Development
31.	Fundamentals of Mechanical Engineering	19ME0349	Skill Development
32.	Linux Programming	19CS0549	Skill Development
33.	Electronic Devices and Circuits Lab	19EC0405	Employability
34.	Switching Theory and Logic Design Lab	19EC0404	Employability
35.	Basic Simulation Lab	19EC0406	Employability
36.	Indian Constitution	19HS0816	Employability
37.	Linear & Digital IC Applications	19EC0409	Employability
38.	Electronic Circuit Analysis	19EC0407	Employability
39.	Analog Communications	19EC0408	Employability
40.	Fundamentals of Urban Planning	19CE0143	Employability
41.	Mechanical Measurements & Control Systems	19ME0350	Employability
42.	Java Programming	19CS0551	Skill Development
43.	Managerial Economics and Financial Analysis	19HS0812	Employability
44.	Electronic Circuit Analysis Lab	19EC0411	Employability
45.	Analog Communications Lab	19EC0413	Employability
46.	Linear & Digital IC Applications Lab	19HS0805	Employability

II M.Tech

	Scripting Language for VLSI Design Automation	19EC4218	Employability
1.	Nano Materials and Nanotechnology	19EC4219	Employability
2.	Wireless Sensor Networks	19EC4008	Employability
3.	Optical Networks	19EC4021	Employability
4.	Testing & Testability	19EC4213	Employability
5.	RF and Microwave Circuit Design	19EC4022	Employability
6.	Advanced Digital System Design	19EC4001	Employability
7.	Radio Frequency Identification	19EC4116	Employability
8.	System on Chip Architecture	19EC4117	Employability
9.	Business Analytics	19HS0824	Entrepreneurship
10.	Cost Management of Engineering Projects	19CE1028	Skill Development
11.	Waste to Energy	19EE2128	Skill Development
12.	Industrial Safety	19ME3121	Skill Development
13.	Advances in Operations Research	19ME3021	Skill Development
14.	Composite Materials	19ME3022	Skill Development
15.	Dissertation Phase-I	19EC4220	Skill Development
16.	DissertationPhase- II	19EC4221	Skill Development
17.			

Modifications described above are carried out to the curriculum after discussions in the BoS by considering the feedback/suggestions from the stakeholders viz. students, alumni, faculty and employers.

Agenda: 3

Approval of course structure and syllabi for III year B.Tech. under R18 Regulation in ECE w.e.f., 2020-2021.

Resolution: 3

After thorough discussion, course structure and syllabus was framed to make the students acquire required technical knowledge and skills. The BOS resolved to approve the course structure for III year B.Tech. under R18 Regulation (given in Annexure –I and II respectively) applicable from the A.Y.2020-21.

Agenda: 4

Approval of course structure and syllabi for I & II years M.Tech. under R20 Regulation and II year M. Tech under R19 regulation.

Resolution:4

The BOS resolved to approve the syllabi framed for I & II years M.Tech. under R20 Regulation and II year M. Tech under R19 regulation (given in Annexure -I and II respectively).

Agenda: 5

Approval of Panel of examiners and Question paper setters for various regulations under UG.

Resolution:5

Approved the panel of examiners prepared for valuation and panel of question paper setters (given in Annexure-IV and V respectively) to be submitted to the college Academic council for approval.

Agenda: 6

Approval of Panel of examiners and Question paper setters for various regulations under PG.

Resolution:6

Approved the panel of examiners prepared for valuation and panel of question paper setters (given in Annexure–IV and V respectively) to be submitted to the college Academic council for approval.

Agenda: 7

Approval of Formulated/Modified Vision, Mission, PEO's and PSO's.

Resolution:7

Approved the Formulated/Modified Vision, Mission, PEO's and PSO's (given in Annexure -VI) to be submitted to the college academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BOS. The meeting was concluded with vote of thanks proposed by the Chairman-BOS.

Members Present

S. No.	Member Name	Designation/Organisation	Role of BOS	Signature
1.	Dr. P. Ratna Kamala	Professor & HOD	Chairman	Maurala 20 20
2.	Dr. P.G.Kuppaswamy	Professor (Signal Processing)	Member	Warralo 20 20 Wodny 20 108/2020
3.	Dr. P.G.Gopinath	Professor (VLSI)	Member	28/8/2022
4.	J. Rajanikanth	Associate Professor (DECS)	Member	J- Ray 28/8/22
5.	P.Pavan Kumar	Assistant Professor (Embedded Systems)	Member	28/8/2020
6.	Dr T. Ramashri	Professor Sri Venkateswara University College of Engineering,	Member	Rusi 24/08/2020
7.	Dr. K.P. Naveen	Assistant Professor, IIT Tirupathi	Member	Marum 38/18/2020
8.	Dr. R.V.S. Satyanarayana	Professor, Sri Venkateswara University College of Engineering,	Member	Rish
9.	Mr. M. Lakshmi Narayana	Junior Telecom Officer, BSNL OFC Transmission maintenance	Member	M. L. N. Jasjost2020
10	Mr V. Prasanth	Software Developer DXC Technologies Chennai.	Member	V Prasanth