

## FACULTY PROFILE



1. Name and Designation : U SRINIVASULU, Associate Professor  
2. Date of Birth : 15/05/1984  
3. Highest Qualification : M.Tech  
4. Academic Performance (Descending Order):

S.No	Degree	University/ Institution	Year of Pass	% of Marks	Class
1	M.Tech	AITS	2010	73.33	I
2	B.Tech	RGM CET	2007	58.09	II
3	INTER	CVSR	2003	72.22	I
4	SSC	GOVT HIGH SCHOOL	2000	52.22	II

5. Total Years of teaching / industry experience till date : 11  
6. Specialization : VLSI SYSTEMS DESIGN  
7. Date of joining in this Department : 01/06/2016  
8. Designation as on date of joining : ASSISTANT PROFESSOR  
9. Present Designation : ASSOCIATE PROFESSOR  
10. Number of promotions since date of joining : 1  
11. Achievements as on 01.06.2022 : Qualified Gate in 2007

S. No.	Achievements	
1	Number of papers published	08
2	Number of papers presented in conferences	-
3	Number of Projects guided	12
4	Number of FDPs / STTPs / Workshop Attended	50
5	Number of National / International Conferences Attended	3
6	Others (Book Chapters / Patents etc) :	-

#### 14. Self - Appraisal:

Major Strengths	Major Weaknesses
1. Flexible and adaptable 2. Hard working, works extra hours	Little short tempered

**Signature of the faculty**