

## FACULTY PROFILE



1. Name and Designation : RAGHUL G  
2. Date of Birth : 12.12.1991  
3. Highest Qualification : M.E  
4. Academic Performance (Descending Order):

S.No	Degree	University/ Institution	Year of Pass	% of Marks	Class
1	M.E	VSBECE	2015	8.5	I Class with Distinction
2	B.E	NPRCET	2013	8.2	I Class
3	HSC	MSPSNMHSS	2009	84.1	I Class
4	SSLC	MSPSNMHSS	2007	90.4	I Class

5. Total Years of teaching / industry experience till date : Teaching Experience: 05 Years 06 Months  
6. Specialization : VLSI Design  
7. Date of joining in this Department : 01.06.2019  
8. Designation as on date of joining : 24.04.2015  
9. Present Designation : Assistant Professor  
10. Number of promotions since date of joining : NIL  
11. Achievements as on 01.06.2022 : NIL

S. No.	Achievements	
1	Number of papers published	02
2	Number of papers presented in conferences	02
3	Number of Projects guided	04
4	Number of FDPs / STTPs / Workshop Attended	05
5	Number of National / International Conferences Attended	02
6	Others (Book Chapters / Patents etc)	Patents: 02

14. Self - Appraisal:

Major Strengths	Major Weaknesses
Hard Work	Overthinking

**Signature of the faculty**