

FACULTY PROFILE

1. Name and Designation : P SARANYA, Assistant Professor
2. Date of Birth : 30/10/1990
3. Highest Qualification : M.Tech
4. Academic Performance (Descending Order):



S.No	Degree	University/ Institution	Year of Pass	% of Marks	Class
1	M.TECH	SITAMS(AUTONOMOUS) JNTUA AFFILIATED	2013	74	FIRST
2	B.TECH	KKC INSTITUTE OF ENGG AND TECHNOLOGY P.R.MANGALAM	2015	8.0(CGPA)	FIRST
3	INTERMEDIATE	SRI SURYA JUNIOR COLLEGE,NAGARI	2009	88.6	FIRST
4	SSC	SRI SARASWATHI VIGNANA MANDIR,NAGARI	2007	89.5	FIRST

5. Total Years of teaching / industry experience till date : 3
6. Specialization : VLSI SYSTEM DESIGN
7. Date of joining in this Department : 01/08/2019
8. Designation as on date of joining : ASSISTANT PROFESSOR
9. Present Designation : ASSISTANT PROFESSOR
10. Number of promotions since date of joining : NIL
11. Achievements as on 01.06.2022 :

S. No.	Achievements	
1	Number of papers published	1
2	Number of papers presented in conferences	NIL
3	Number of Projects guided	2
4	Number of FDPs / STTPs / Workshop Attended	7
5	Number of National / International Conferences Attended	NIL
6	Others (Book Chapters / Patents etc) :	NIL

14. Self - Appraisal:

Major Strengths	Major Weaknesses
1. Flexible and adaptable 2. Hard worker 3.Honesty 4.Self motivated	1.Over thinking 2.Impatience

Signature of the faculty