

## FACULTY PROFILE



1. Name and Designation : P.ALEKYA  
2. Date of Birth : 19/08/1992  
3. Highest Qualification : M.TECH  
4. Academic Performance (Descending Order):

S.No	Degree	University/ Institution	Year of Pass	% of Marks	Class
1	M.TECH	SREERAMA ENGINEERING COLLEGE, TIRUPATI	2015	78	FIRST
2	B.TECH	SITS, TIRUPATI	2013	73	FIRST
3	INTERMEDIATE	SRI CHAITANYA MAHILAKALASALA, TIRUPATI	2009	89.8	FIRST
4	SSC	Z P P HIGH SCHOOL KAMMAKANDRIGA	2007	85.5	FIRST

5. Total Years of teaching / industry experience till date : 3  
6. Specialization : VLSI DESIGN  
7. Date of joining in this Department : 08/07/2019  
8. Designation as on date of joining : ASSISTANT PROFESSOR  
9. Present Designation : ASSISTANT PROFESSOR  
10. Number of promotions since date of joining : NIL  
11. Achievements as on 01.06.2022 :

S. No.	Achievements	
1	Number of papers published	NIL
2	Number of papers presented in conferences	NIL
3	Number of Projects guided	01
4	Number of FDPs / STTPs / Workshop Attended	05
5	Number of National / International Conferences Attended	NIL
6	Others (Book Chapters / Patents etc) :	NIL

12. Self - Appraisal:

Major Strengths	Major Weaknesses
1. Self confidence 2. Patience	NIL

**Signature of the faculty**