

FACULTY PROFILE

1. Name and Designation : Dr. P.G.Gopinath
2. Date of Birth : 02.03.1984
3. Highest Qualification : Ph.D.
4. Academic Performance (Descending Order):



S.No	Degree	University/ Institution	Year of Pass	% of Marks	Class
1	Ph.D.	JNTUA, Anantapur	2019	-	
2	M.E.	Anna University, Chennai	2011	8.2	I Class
3	B.E.	Anna University, Chennai	2005	79	I Class with Distinction

5. Total Years of teaching / industry experience till date : 15
6. Specialization : VLSI DESIGN
7. Date of joining in this Department : 26/08/2019
8. Designation as on date of joining : Professor
9. Present Designation : Professor
10. Number of promotions since date of joining : 02
11. Achievements as on 01.06.2022 :

S. No.	Achievements	
1	Number of papers published	25
2	Number of papers presented in conferences	5
3	Number of Projects guided	15
4	Number of FDPs / STTPs / Workshop Attended	12
5	Number of National / International Conferences Attended	2
6	Others (Book Chapters / Patents etc) :	1

14. Self - Appraisal:

Major Strengths	Major Weaknesses
1. Flexible and adaptable 2. Hard worker 3. Honesty 4. Self motivated	1. Over thinking 2. Impatience

Signature of the faculty