SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR <u>FACULTY PROFILE</u>

Personal Details		
Name	: Dr.K.Muralikumar	
Date of Birth	: 15-10-1986	
Address	: 5-5, Teachers colony, Pachikapallam,	
	Vedhurukuppam, Chittoor Dist, AP, 517569.	

Date of Joining in this institute

:01-07-2014

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Academic Details									
Qualification	Specialization		Name of the Board/University				Year of Passing		
Ph.D.	Power Electronics		Vellore Institute of Technology (VIT), Vellore				April,2022		
M.Tech.	Power Electronics and Electrical Drives		Sri Venkatesa Engineering a (JNTUA, Ana	Oct, 2013					
B.Tech.	EEE		Sri Venkatesa Perumal College of Engineering and Technology, Puttur. (JNTU, Hyderabad).				April,2006		
Professional E	xperience								
Designation			Name of the Employer				Length of Service		
Associate Protessor			Siddha <mark>rtha Institute</mark> of Engineering & Technology, Puttur						
Assistant Professor		Siddhartha Institute of Engineering & 7Y 11M Technology, Puttur							
Assistant Professor Swe		Sweth	etha Institute of science and Technology for 1Y 10M men, Tirupati.						
Professional N	lembershir								
> SCRS	- W	1.11		IAS	STER				
> IAENG			>	GI			/		
Research Publ	ications								
National Journals	Internat Journ		National Conferences		ernational nferences	Books / Book Chapters	Patents		
_	8		2		15	3	2		
Seminars/Wor	kshops/FD	Ps/NPT	EL attended						
Seminars			Workshops		FDPs		NPTEL		
20			15		20				
Other Achievements									
• Reviewer for the Advances in Science, Technology and Engineering Systems Journal									

• Reviewer for the 8th International Conference Soft Computing for Problem Solving - SocProS 2018, VIT.

• Reviewer for the 2nd IEEE International Conference on Innovations in Power and Advanced Computing Technologies, i-PACT 2019,i-PACT 2020, VIT, Vellore.