

**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR**  
**FACULTY PROFILE**

**Personal Details**

**Name** : Dr. M. PRIYA  
**Date of Birth** : 06-04-1991  
**Address** : 5-5, Teachers Colony, Pachikapallam, Chittoor  
 Dist, Andhra Pradesh,517569, India



**Date of Joining in this institute** : 29-03-2023

**Academic Details**

Qualification	Specialization	Name of the Board/University	Year of Passing
Ph.D.	Power electronics	Vellore Institute of Technology (VIT), vellore, TamilNadu	Feb, 2022
M.Tech.	Power Electronics and Electrical Drives	Siddhartha Institute of Science and Technology, Puttur, Andhra Pradesh, JNTUA, Anantapuramu.	Nov, 2014
B.Tech.	EEE	Swetha Institute of Science and Technology for Womens, Ramapuram, Tirupati. JNTUA, Anantapuramu.	March, 2012

**Professional Experience**

Designation	Name of the Employer	Length of Service
Associate Professor	Siddharth Institute of Engineering & Technology, Puttur	29-03-2023 to till Date

**Professional Memberships**

SCRS

**Research Publications**

National Journals	International Journals	National Conferences	International Conferences	Books / Book Chapters	Patents
-	04	-	08	01	2

**Seminars/Workshops/FDPs/NPTEL attended**

Seminars	Workshops	FDPs	NPTEL
05	14	08	--

**Other Achievements**

- Reviewer for the Advances in Science, Technology and Engineering Systems Journal
- Reviewer for the 8th International Conference Soft Computing for Problem Solving - SocProS 2018, VIT.
- Reviewer for the 2nd IEEE International Conference on Innovations in Power and Advanced Computing Technologies, i-PACT 2019, VIT, Vellore.
- Reviewer for the 2nd IEEE International Conference on Innovations in Power and Advanced Computing Technologies, i-PACT 2020, VIT, Vellore.