SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR $\underline{FACULTY\ PROFILE}$

Personal Details

Name : Boggala Ramesh

Date of Birth : 30-06-1986

Address : Karlapudi(V), KVB Puram (M),

Thirupathi Dt, 517643

Date of Joining in this institute : 01-06-2012



Academic Details						
Qualification	Specialization		Name of the Board/University			Year of Passing
M.Tech.	Electrical Power System		J.N. <mark>T Universit</mark> y, Anantapur			Sep,2012
B.Tech.	EEE		J.N.T University, Anantapur.		April,2010	
Professional Experience						
Designation			Name of the Employer			Length of Service
Accierant Protector			harth I <mark>ns</mark> titute of <mark>E</mark> ngineering & nolo <mark>gy, Puttur</mark>			01-06-2012 to till date
Professional Memberships						
						()-5
Research Publications						
National Journals	International Journals		National Conferences	International Conferences	Books / Book Chapters	Patents
11.47					11 Y	1/
Seminars/Workshops/FDPs/NPTEL attended						
Seminars			Workshops FDPs		NPTEL	
2 10 15						<u></u>
Other Achievements						
PITTIP /						

EST D: 2001