

SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR
(AUTONOMOUS)

11th BoS Meeting of Electronics and Communication Engineering (ECE)

Date: 20/07/2024

The 11th meeting of Board of Studies (BoS) in Electronics and Communication Engineering is held on 20th July, 2024 (Saturday) at 10.00AM online through ZOOM platform.

As per UGC (University Grant Commission) guidelines, the Choice Based Credit System(CBCS) and elective have been implemented in the curriculum.

Dr. P. Ratna Kamala, Chairman - BoS chaired the meeting and welcomed all the members to the eleventh BoS meeting. The following agenda points are discussed:

Agenda

1. Approval of course structure and syllabi for II year B.Tech. under R23 Regulation.
2. Approval of Panel of examiners and Question paper setters for II B.Tech. that comes under R23 Regulation.
3. Any other item with the permission of Chair.

After a brief introduction of the agenda items listed above, each agenda item were taken up for discussion and the following resolutions were passed.

Minutes:

Agenda: 1

Approval of course structure and syllabi for II year B.Tech. under R23 Regulation in ECE w.e.f., 2024-2025.

Resolution: 1

After detailed discussion, the members of BOS resolved to approve course structure and syllabi for II year B.Tech. Under R23 Regulation (given in **Annexure – I & II respectively**) applicable for the A.Y.2024-2025.

A. Course & Syllabus Comparison

With reference to the R20 regulations, the new regulation (R23) syllabus for II year has the following modifications which are given in the below table.

II B.Tech

S.No	R20 Regulation	R23 Regulation	Percentage of course content changed
1.		Probability and Complex Variables	100
2.		Universal Human Values– Understanding Harmony and Ethical Human Conduct	100
3.	Signals, Systems and Random Processes	Signals, Systems and Stochastic Processes	20
4.	Electronic Devices and Circuits	Electronic Devices and Circuits	20
5.	Switching Theory and Logic Design	Digital Circuits Design	40
6.	Electronic Devices & Logic Design Lab	Electronic Devices and Circuits Lab	0
7.	Signals and Systems Simulation Lab	Digital Circuits & Signal Simulation Lab	50
8.		Python Programming	100
9.	Environmental Science	Environmental Science (Audit Course)	100
10.		Managerial Economics and Financial Analysis Organizational Behavior Business Environment	100
11.	Control Systems	Linear Control Systems	0
12.		EM Waves and Transmission Lines	100
13.	Electronic Circuit Analysis	Electronic Circuits Analysis	100
14.	Analog Communications Digital Communications	Analog and Digital Communications	0
15.	Electronic Circuit Analysis Lab	Electronic Circuits Analysis Lab	0

16.	Analog Communications Lab Digital Communications Lab	Analog and Digital Communications Lab	0
17.		Soft Skills	100
18.		Design Thinking and Innovation	100

Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
ECE B.Tech II Year	18	57.22%

B. Course Relevance

The courses that come under the category of Employability, Skill or Entrepreneurship Development are shown in the table below.

II B.Tech

S.No	Course Title	Course Code	Relevance
1.	Probability and Complex Variables	20HS0812	Skill Development
2.	Universal Human Values-- Understanding Harmony and Ethical Human Conduct	20EC0436	Employability
3.	Signals, Systems and Stochastic Processes	20EC0437	Skill Development
4.	Electronic Devices and Circuits	20EC0438	Skill Development
5.	Digital Circuits Design	20EC0439	Skill Development
6.	Electronic Devices and Circuits Lab	20EC0440	Skill Development
7.	Digital Circuits& Signal Simulation Lab	20EC0441	Skill Development
8.	Python Programming	20EC0442	Skill Development
9.	Environmental Science (Audit Course)	20EC0443	Skill Development
10.	Managerial Economics and Financial Analysis Organizational Behavior Business Environment	20EC0444	Entrepreneurship

11.	Linear Control Systems	20CE0148	Skill Development
12.	EM Waves and Transmission Lines	20EE0237	Skill Development
13.	Electronic Circuits Analysis	20ME0356	Skill Development
14.	Analog and Digital Communications	20CS0511	Skill Development
15.	Electronic Circuits Analysis Lab	20HS0861	Skill Development
16.	Analog and Digital Communications Lab	20CE0171	Skill Development
17.	Soft Skills	20EE0239	Skill Development
18.	Design Thinking and Innovation	20ME0357	Skill Development

Modifications described above are carried out to the curriculum after discussions in the BoS by considering the feedback/suggestions from the stakeholders viz. students, alumni, faculty and employers.

As per the decisions taken in the meeting and considering the suggestions from the suggestions or feedback from the stakeholders for the above modifications in the curriculum.

Agenda: 2


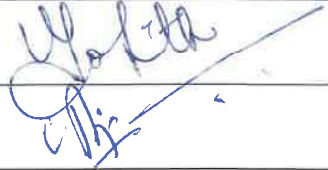

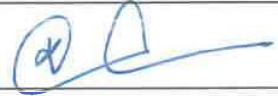



Approval of Panel of examiners and Question paper setters for II B.Tech. that comes under R23 Regulation.

Resolution: 2

Approved the panel of examiners prepared for valuation and panel of question paper setters (given in **Annexure - III and IV** respectively) under R23 Regulation to be submitted to the college Academic council for approval.

The above items were discussed and the necessary approval was accorded by the BOS. The meeting was concluded with vote of thanks proposed by the Chairman-BOS.

Members Present

S.No.	Member Name	Designation/Organization	Role of BOS	Signature
1.	Dr. P. Ratna Kamala	Professor & Head	Chairman	
2.	Dr. P. G. Gopinath	Professor	Member	
3.	Dr. C. Priya	Professor	Member	
4.	J. Rajanikanth	Associate Professor	Member	J. Raja
5.	K.D. Mohana Sundaram	Associate Professor	Member	
6.	Dr T. Ramashri	Professor, ECE Sri Venkateswara University College of Engineering, Tirupati.	Member	<u>Ramashri</u>
7.	Dr R.V.S. Satyanarayana	Professor, ECE Sri Venkateswara University College of Engineering, Tirupati.	Member	
8.	Dr. P. Ramana Reddy	Professor, ECE JNTUA college of Engineering, Ananthapuramu.	Member	
9.	Mr. M. Lakshmi Narayana	Sub-Divisional Engineer, Cellone (O&I), Mobile Service, BSNL.	Member	
10.	Mr V. Prasanth	Software Developer DXC Technologies Chennai.	Member	<u>V. Prasanth</u>