

**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR
(AUTONOMOUS)**

2nd BoS Meeting of Electronics and Communication Engineering (ECE)

Date: 23/12/2017

The 2nd meeting of Board of Studies (BoS) in Electronics and Communication Engineering is held on 23rd December 2017 at 10.00 AM in the Department of Electronics and Communication Engineering, Siddharth Institute of Engineering & Technology, Puttur, Chittoor –Dist.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum

Mr. M. Janardhan Raju, Chairman - BoS chaired the meeting and welcomed all the members to the Second BoS meeting and discussed about the following agenda:

1. Preparation of course structure for III & IV-year UG in ECE w.e.f., 2018-19.
2. Preparation of syllabi for III & IV-year UG in ECE w.e.f., 2018-19.
3. Preparation of syllabus for the subject offered to other branches w.e.f., 2018-19.
4. Suggesting Panel of Question Paper setters.
5. Suggesting Panel of Examiners.
6. Quality of internal Question papers.
7. Academic project evaluation.

After a brief introduction of the agenda items listed above, each agenda item was taken up for discussion and the following resolutions were passed.

Minutes:

Agenda: 1

Preparation of course structure for III & IV-year UG in ECE w.e.f., 2018-19.

Resolution: 1

After the detailed discussion the course structure for III & IV-year UG in ECE was prepared (given in **Annexure –I**) and is applicable from the A.Y., 2018-19.

Agenda: 2

Preparation of syllabus for III & IV-year UG in ECE w.e.f., 2018-19.

Resolution: 2

After the thorough discussion syllabi was formulated to make the students acquire the required technical knowledge and skills. The syllabi framed for the III and IV years of UG in ECE (given in **Annexure –II**) and is applicable from the A.Y., 2018-19.

A. Course & Syllabus Comparison

With reference to the R15 regulations, the new regulation (R16) syllabus for 3rd & 4th year has the following modifications, which are given in the below table.

III & IV B.Tech.

S.No	R15 Regulation	R16 Regulation	Percentage of course content changed
1.		Pulse & Digital Circuits	100
2.		Pulse & Digital Circuits Lab	100
3.	Electronic Circuit Analysis Laboratory	Electronic Circuit Analysis Lab	0
4.		Electrical Technology Lab	0
5.	Computer Organization	Computer Organization and Architecture	0
6.	Antennas and Wave Propagation	Antennas & Wave Propagation	60
7.		Linear Control Systems	100
8.	Digital Communication Systems	Digital Communications	0

9.	Linear Integrated Circuits and Applications	Linear IC Applications	25
10.		Analog Communications Lab	100
11.	Digital Communication Systems Laboratory	Digital Communications Lab	0
12.	Managerial Economics and Financial Analysis	Managerial Economics & Financial Analysis	0
13.		Digital IC Applications	100
14.	Microprocessors & Microcontrollers	Microprocessors & Microcontrollers	80
15.		Analog Communications	100
16.	Electronic Measurements and Instrumentation	Electronic Measurements and Instrumentation	0
17.	Digital Signal Processing	Digital Signal Processing	20
18.	VLSI Design	VLSI Design	50
19.	Neural Networks & Fuzzy Logic	Neural Networks & Fuzzy Logic	35
20.	Intellectual Property Rights	Intellectual Property Rights	0
21.		Digital IC Applications Lab	100
22.	Advanced English Language Communication Skills (AELCS) Laboratory (Audit Course)	Advanced English Language and Communication Skills Lab	0
23.	Optical Fiber Communication	Optical Fiber Communication	25
24.		Entrepreneurship Development	100
25.	Embedded Systems	Embedded Systems	90
26.	Microwave Engineering	Microwave Engineering	10
27.	Data Communications and Networking	Computer Networks	55
28.	Radar Systems	Radar & Navigational Aids	65
29.		Satellite and TV Engineering	100
30.		Spread spectrum communications	100
31.	Digital Image Processing	Digital Image processing	10
32.		Medical Electronics	100
33.		Elements of Road Traffic Safety	100

34.		Non-Conventional Energy Resources	100
35.		Database Management systems	100
36.		Wireless Communication & Networks	100
37.	Cellular & Mobile Communication	Cellular & Mobile Communication	100
38.	Real Time Systems	Real time operating Systems	0
39.	Microwave and Optical Communication Laboratory	Microwave & Optical Communications Lab	0
40.	VLSI & Embedded Systems Laboratory	Embedded Systems Lab	100
41.	Pattern Recognition & Applications	Pattern Recognition & Applications	0

Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
ECEB. Tech. III and IV Year	41	54.26

B. Course Relevance

The courses that come under the category of Employability, Skill or Entrepreneurship Development are shown in the table below.

S. No.	Course Title	Course Code	Relevance
1	Analog Communications	16EC415	Employability
2	Electronic Measurements and Instrumentation	16EC416	Employability
3	Linear IC Applications	16EC417	Employability
4	Antennas & Wave Propagation	16EC418	Employability
5	Linear Control Systems	16EE216	Employability
6	Managerial Economics & Financial Analysis	16MB750	Entrepreneurship
7	Analog Communications Lab	16EC419	Employability
8	Linear IC Applications Lab	16EC420	Employability
9	Digital Communications	16EC421	Employability
10	Digital Signal Processing	16EC422	Employability
11	Microprocessors & Microcontrollers	16EC423	Employability
12	Digital IC Applications	16EC424	Employability
13	Microwave Engineering	16EC425	Employability
14	Advanced English Language and Communication Skills Lab	16HS615	Skill Development

15	Digital Communications Lab	16EC426	Skill Development
16	Digital IC Applications Lab	16EC427	Skill Development
17	Entrepreneurship Development	16MB751	Entrepreneurship
18	Embedded Systems	16EC429	Employability
19	Optical Fiber Communication	16EC430	Employability
20	VLSI Design	16EC431	Employability
21	Digital Image processing	16EC432	Employability
22	Medical Electronics	16EC433	Employability
23	Pattern Recognition & Applications	16EC434	Employability
24	Elements of Road Traffic Safety	16CE145	Employability
25	Neural Networks & Fuzzy Logic	16EE239	Employability
26	Non-Conventional Energy Resources	16ME313	Employability
27	Database Management systems	16CS511	Employability
28	Intellectual Property Rights	16MB752	Entrepreneurship
29	Microwave & Optical Communications Lab	16EC435	Employability
30	Embedded Systems Lab	16EC436	Employability
31	Real time operating Systems	16EC437	Employability
32	Radar & Navigational Aids	16EC438	Employability
33	Satellite and TV Engineering	16EC439	Employability
34	Spread spectrum communications	16EC440	Employability
35	Wireless Communication & Networks	16EC441	Employability
36	Computer Networks	16CS527	Employability
37	Cellular & Mobile Communication	16EC442	Employability

Modifications described above are carried out to the curriculum after discussions in the BoS by considering the feedback/suggestions from the stakeholders viz. students, alumni, faculty and employers.

Agenda: 3

Preparation of syllabus for the subject offered to other branches w.e.f. 2018-19.

Resolution: 3

After the thorough discussion syllabus was prepared for the subject offered to other branches (given in **Annexure-III**) and is applicable from the A.Y., 2018-19.

Agenda: 4

Suggesting Panel of question paper setters.

Resolution: 4

The panel of question paper setters was suggested (given in **Annexure -IV**).

Agenda: 5

Suggesting Panel of examiners.

Resolution: 5

The panel of examiners for valuation was suggested(given in **Annexure –V**).

The above items were discussed, debated and the necessary approval was accorded by the BoS. The meeting was concluded with vote of Thanks proposed by the Chairman-BoS.

Agenda: 6

Quality of internal Question papers.

Resolution: 6

After sample internal question papers verification panel suggested to prepare the questions matching with the course outcomes and also advised to follow Blooms taxonomy levels.

Agenda: 7

Academic project evaluation.

Resolution: 7

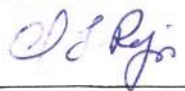



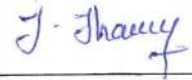


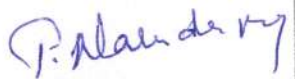
The panel revised the rubric of project evaluation and instructed to following parameters.

Significance of the project, Literature survey, knowledge in the domain, presentation and implementation.

2nd BoS Meeting of Electronics and Communication Engineering (ECE)

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Members Present

S.No.	Member Name	Academic/ Industry Position	Role in the BOS	Signature
1.	Prof. M.Janardhana Raju	Professor & HOD-SIETK	Chairman	
2.	Mrs. K.S.Devesawari	Associate Professor-SIETK (Applied electronics)	Member	
3.	Mr. V.Viswanadha	Associate Professor-SIETK (DECS)	Member	
4.	Mr. C. Vijaya Bhaskar	Associate Professor-SIETK (VLSI system design)	Member	
5.	Mrs.J.Jhansi	Associate Professor-SIETK (Embedded systems)	Member	
6.	Dr. S. Narayana Reddy	Professor, S.V. University, Tirupati	Member	
7.	Dr. Rama Komaragiri	Associate Professor, NIT Calicut	Member	ABSENT
8.	Dr.P.Ramana Reddy	Professor JNTUA, Ananthapuramu	Member	
9.	Mr. Narendra Reddy	Scientist-C, CMTI, Bangalore	Member	
10.	Mr. B.Venkatadri	Software Developer HCL Technologies	Member	ABSENT