

**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR**  
**(AUTONOMOUS)**

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**3<sup>rd</sup> BOS Meeting of Electronics & Communication Engineering (ECE)**

Date: 18-06-2018

The 3<sup>rd</sup> meeting of Board of Studies (BOS) in Electronics & Communication Engineering is held on 18 June, 2018 at 2:00 PM in the Department of Electronics & Communication Engineering, Siddharth Institute of Engineering & Technology, Puttur, Chittoor –Dist.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. M. Janardhana Raju, Chairman BOS chaired the meeting and welcomed all the members to the third BOS meeting and discussed about the following agenda

**Agenda:**

1. Approval of course structure for UG & PG in ECE w.e.f., A.Y.2018-19.
2. Approval of syllabus for I & II-year UG in ECE w.e.f., A.Y.2018-19.
3. Approval of syllabus PG in ECE w.e.f., A.Y.2018-19
4. Approval of syllabus for the subjects offered to various branches w.e.f. 2018-19.
5. Approval of Panel of Question Paper setters.
6. Approval of Panel of Examiners.
7. Procedures of Evaluation.
8. Academic facilities improvement.

After a brief introduction the agenda items listed above were taken up for discussion and the following resolutions were passed.

**Minutes:**

**Agenda: 1**

Approval of course structure for UG & PG in ECE w.e.f., A.Y.2018-19

**Resolution: 1**

After detailed discussion the BOS resolved to approve the course structure for UG & PG(ECE) (given in **Annexure –I**) applicable from the A.Y.2018-19.

**Agenda: 2**

Approval of syllabus for I & II-year UG in ECE w.e.f., A.Y.2018-19

**Resolution: 2**

After the thorough discussion syllabus was framed to make the students acquire the required technical knowledge and skills. The BOS resolved to approve the syllabi framed for the I and II-year B. Tech I & II-Semesters (given in **Annexure-II**)

**A. Course & Syllabus Comparison**

With reference to the R16 regulations, the new regulation (R18) syllabus for I<sup>st</sup> and 2<sup>nd</sup> year has the following modifications, which are given in the below table.

**I & II B.Tech**

S.No	R16 Regulation	R18 Regulation	Percentage of course content changed
1.	Functional English	English	10
2.	Engineering Mathematics-I	Mathematics-I	0
3.	Engineering Chemistry	Chemistry	40
4.	Computer Programming	Programming For Problem Solving	100
5.	Engineering Graphics	Engineering Graphics & Design	0
6.	English Language and Communication Skills Lab	English Lab	0
7.	Engineering & IT workshop Lab	Workshop Practice Lab	0

8.	Engineering Chemistry Lab	Chemistry lab	40
9.	Computer Programming Lab	Programming For Problem Solving Lab	100
10.	Engineering Mathematics-II	Mathematics-II	0
11.	Engineering Physics	Semiconductor Physics	20
12.	Network Analysis	Network Theory	100
13.		Basic Electrical Engineering	100
14.		Engineering Mechanics	100
15.		Essence of Indian Traditional Knowledge	0
16.	Engineering Physics Lab	Physics Lab	0
17.	Engineering Mathematics-III	Mathematics-III	0
18.	Basic Electronic Devices	Electronic Devices	60
19.	Switching Theory & Logic Design	Digital System Design	40
20.	Signals and Systems	Signals & Systems	0
21.	Random Signal & Stochastic Processes	Probability Theory and Stochastic Processes	0
22.	Environmental Studies	Environmental Sciences	0
23.		Digital System Design Lab	100
24.	Basic Electronic Devices Lab	Electronic Devices Lab	0
25.	Basic Simulation Lab	Signals and Systems Simulation Lab	0
26.		Indian Constitution	0
27.	Electronic Circuit Analysis	Analog Circuits	45
28.	Electronic Circuit Analysis Lab	Analog Circuits Lab	100
29.	Analog Communications	Analog Communications	0
30.	Comprehensive Online Examination-II	Aptitude Practices	0
31.	Comprehensive Soft Skills	Comprehensive Online Examination-II	0
32.		Biology for Engineers	100
33.	Managerial Economics & Financial Analysis	Managerial Economics & Financial Analysis	0
34.	Analog Communications Lab	Analog Communications Lab	0

**Consolidated Sheet**

<b>Course</b>	<b>Total courses</b>	<b>Percentage of syllabus changed</b>
<b>ECE B.Tech I &amp; II Year</b>	<b>34</b>	<b>31.02</b>

**I & II M.Tech**

<b>S.No</b>	<b>R16 Regulation</b>	<b>R18 Regulation</b>	<b>Percentage of course content changed</b>
1.	Digital System Design	Advanced Digital Signal Processing	0
2.	Advanced Digital Signal Processing	Digital Communication Techniques	0
3.	DSP Processors & Architectures	DSP Processors & Architectures	0
4.	Hi-Speed Networks	High Speed Networks	0
5.		Antenna and Radiating Systems	0
6.		Voice and Data Networks	0
7.		Wireless Sensor Networks	0
8.		Advanced Digital Signal Processing Lab(Virtual Lab)	100
9.		Advanced Digital System Design Lab	100
10.		Research Methodology and IPR	100
11.		English for Research Paper Writing	100
12.		Disaster Management	100
13.		Sanskrit for Technical Knowledge	100
14.		Advanced Digital System Design	100
15.		Wireless Communications	100
16.		Coding Theory & Techniques	100
17.		Introduction to IoT	100
18.		Adaptive Signal Processing	100
19.		Cognitive Radio	100
20.		Image & Video Processing	100
21.		Pattern Recognition and Machine learning	100
22.		Detection & Estimation of Signals	100

23.		Advanced Communications Lab (Virtual Lab)	100
24.		Image & Video Processing Lab	100
25.		Mini Project	100
26.		Constitution of India	100
27.		Pedagogy Studies	100
28.		Stress Management by Yoga	100
29.		Personality Development Through Life	100
30.		Enlightenment Skills.	100
31.	Micro Controllers & Interfacing	Advanced Microcontrollers	100
32.	Embedded System Concepts	Embedded System Design	0
33.		Sensors and Actuators	100
34.		FPGA Architectures & Applications	100
35.		Embedded Networking	100
36.		Wireless Communications	100
37.		Internet Protocols	100
38.		Embedded System Design Lab	100
39.		Structural Digital System Design Lab	100
40.		Research Methodology and IPR	100
41.		English for Research Paper Writing	100
42.		English for Research Paper Writing	100
43.		Disaster Management	100
44.		Sanskrit for Technical Knowledge	100
45.		Value Education	100
46.		Introduction to IoT	100
47.	Digital System Design	Structural Digital System Design	0
48.	Digital IC Design	Digital IC Design	100
49.	VLSI Technology	VLSI Technology	0
50.		Wireless Sensor Networks	0
51.		Internet of Things Lab	100

52.	Microcontrollers & Interfacing Lab	Microcontrollers & Interfacing Lab	100
53.	Real Time Operating Systems	Real Time Operating Systems	0
54.	Testing & Testability	Testing & Testability	100
55.	Hardware Software Co-Design	Hardware Software Co-Design	10
56.		Mini Project	0
57.		Constitution of India	0
58.		Pedagogy Studies	100
59.		Stress Management by Yoga	100
60.		Personality Development Through Life Enlightenment Skills.	100
61.		Advanced Digital Signal Processing	100
62.		System on Chip Architecture	100
63.		Business Analytics	100
64.		Industrial Safety	0
65.		Advanced Operations Research	100
66.		Cost Management of Engineering Projects	100
67.		Composite Materials	100
68.		Waste to Energy	100
69.	FPGA Architecture and Applications	FPGA Architectures & Applications	100
70.	Radio Frequency Identification	Radio Frequency Identification	100
71.	Micro Electromechanical Systems	Micro Electromechanical Systems	100
72.		Dissertation-I	0
73.	Project work	Dissertation –II	30
74.	VLSI Technology	VLSI Technology	0
75.	Analog IC Design	Analog IC Design	0
76.	Digital IC Design	Digital IC Design	0
77.	Verilog HDL	Verilog HDL	0
78.	Embedded system Concepts	Embedded System Design	0
79.	System Modeling & Simulation	System Modeling & Simulation	0

80.	ASIC Design	ASIC Design	0
81.		Image & Video Processing	100
82.		Digital Electronic Circuits Lab (Virtual Lab)	100
83.		Research Methodology and IPR	100
84.	Digital IC Design Lab	Digital IC Design Lab	0
85.		English for Research Paper Writing	100
86.		Disaster Management	100
87.		Sanskrit for Technical Knowledge	100
88.		Value Education	100
89.	FPGA Architectures & Applications	FPGA Architectures & Applications	0
90.	Testing & Testability	Testing & Testability	0
91.	Low Power VLSI Design	Low Power VLSI Design	0
92.	Algorithms for VLSI Design Automation	Algorithms for VLSI Design Automation	0
93.		Advanced Digital System Design	100
94.		Solid State Device Modeling and Simulation	100
95.		Digital VLSI Design Lab (Virtual Lab)	100
96.		Mini Project	100
97.		Constitution of India	100
98.		Pedagogy Studies	100
99.		Personality Development Through Life Enlightenment Skills.	100
100.		Stress Management by Yoga	100
101.	Scripting Language for VLSI Design Automation	Scripting Language for VLSI Design Automation	96
102.	Nano Electronics	Nano Electronics	0
103.	Real Time Operating Systems	Real Time Operating System	,
104.	Mixed Signal Lab	Real Time Operating System	0
105.		Scripting Language for VLSI Design Automation	100
106.		Nano Materials and Nanotechnology	100

107.		Wireless Sensor Networks	100
108.		Business Analytics	100
109.		Industrial Safety	100
110.		Advanced Operations Research	100
111.		Cost Management of Engineering Projects	100
112.		Composite Materials	100
113.		Waste to Energy	100

#### Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
ECE M.Tech I& II Year	113	71.75

#### B. Course Relevance

The courses that come under the category of Employability, Skill or Entrepreneurship Development are shown in the table below.

#### I & II B.Tech

S.No	Course Title	Course Code	Relevance
1.	English Lab	18HS0811	Skill Development
2.	Basic Electrical Engineering	18EE0239	Employability
3.	Programming For Problem Solving	18CS0501	Skill Development
4.	Programming For Problem Solving Lab	18CS0503	Employability
5.	Electronic Devices	18EC0401	Employability
6.	Digital System Design	18EC0402	Employability
7.	Signals & Systems	18EC0403	Employability
8.	Network Theory	18EE0242	Employability
9.	Electronic Devices Lab	18EC0404	Employability
10.	Digital System Design Lab	18EC0405	Employability
11.	Signals and Systems Simulation Lab	18EC0406	Employability
12.	Analog Circuits	18EC0407	Employability
13.	Analog Communications	18EC0408	Employability



14.	Managerial Economics and Financial Analysis	18HS0812	Entrepreneurship
15.	Analog Circuits Lab	18EC0410	Employability
16.	Analog Communications Lab	18EC0411	Employability

### I & II M.Tech

S.No	Course Title	Course Code	Relevance
1.	Advanced Digital Signal Processing	18EC4002	Employability
2.	Radio Frequency Identification	18EC4116	Employability
3.	System on Chip Architecture	18EC4117	Employability
4.	Business Analytics	18HS0824	Entrepreneurship
5.	Industrial Safety	18ME3121	Employability
6.	Advanced Operations Research	18ME3122	Employability
7.	Cost Management of Engineering Projects	18CE1028	Skill Development
8.	Composite Materials	18ME3123	Skill Development
9.	Waste to Energy	18EE2128	Skill Development
10.	Dissertation-I	18EC4118	Skill Development
11.	Dissertation -II	18EC4119	Skill Development
12.	Embedded System Design	18EC4101	Employability
13.	Sensors and Actuators	18EC4102	Employability
14.	Structural Digital System Design	18EC4103	Employability
15.	FPGA Architectures & Applications	18EC4209	Employability
16.	Real Time Operating Systems	18EC4104	Employability
17.	Embedded Networking	18EC4105	Employability
18.	Wireless Communications	18EC4011	Employability
19.	Internet Protocols	18EC4106	Employability
20.	Embedded System Design Lab	18EC4107	Skill Development
21.	Structural Digital System Design Lab	18EC4108	Skill Development
22.	Research Methodology and IPR	18HS0823	Skill Development
23.	English for Research Paper Writing	18HS0818	Skill Development
24.	Disaster Management	18CE1029	Skill Development
25.	Sanskrit for Technical Knowledge	18HS0825	Skill Development
26.	Value Education	18HS0826	Skill Development

27.	Introduction to IoT	18EC4109	Employability
28.	Advanced Microcontrollers	18EC4110	Employability
29.	Hardware Software Co-Design	18EC4111	Employability
30.	Testing & Testability	18EC4213	Employability
31.	Micro Electromechanical Systems	18EC4112	Employability
32.	VLSI Technology	18EC4201	Employability
33.	Digital IC Design	18EC4202	Employability
34.	Wireless Sensor Networks	18EC4008	Employability
35.	Internet of Things Lab	18EC4113	Skill Development
36.	Microcontrollers & Interfacing Lab	18EC4114	Skill Development
37.	Mini Project	18EC4115	Skill Development
38.	Constitution of India	18HS0829	Skill Development
39.	Pedagogy Studies	18HS0827	Skill Development
40.	Stress Management by Yoga	18HS0828	Skill Development
41.	Personality Development Through Life Enlightenment Skills	18HS0819	Skill Development
42.	Optical Networks	18EC4021	Employability
43.	Testing & Testability	18EC4213	Employability
44.	RF and Microwave Circuit Design	18EC4022	Employability
45.	Business Analytics	18HS0824	Entrepreneurship
46.	Industrial Safety	18ME3121	Employability
47.	Advanced Operations Research	18ME3122	Employability
48.	Cost Management of Engineering Projects	18CE1028	Employability
49.	Composite Materials	18ME3123	Skill Development
50.	Waste to Energy	18EE2128	Skill Development
51.	Dissertation-I	18EC4023	Skill Development
52.	Dissertation -II	18EC4024	Skill Development
53.	Advanced Digital System Design	18EC4001	Employability
54.	Advanced Digital Signal Processing	18EC4002	Employability
55.	Antenna and Radiating Systems	18EC4003	Employability
56.	Digital Communication Techniques	18EC4004	Employability
57.	DSP Processors & Architectures	18EC4005	Employability
58.	High Speed Networks	18EC4006	Employability

59.	Voice and Data Networks	18EC4007	Employability
60.	Wireless Sensor Networks	18EC4008	Employability
61.	Advanced Digital Signal Processing Lab(Virtual Lab)	18EC4009	Skill Development
62.	Advanced Digital System Design Lab	18EC4010	Skill Development
63.	Research Methodology and IPR	18HS0823	Skill Development
64.	English for Research Paper Writing	18HS0818	Skill Development
65.	Disaster Management	18CE1029	Skill Development
66.	Sanskrit for Technical Knowledge	18HS0825	Skill Development
67.	Value Education	18HS0826	Skill Development
68.	Wireless Communications	18EC4011	Employability
69.	Coding Theory & Techniques	18EC4012	Employability
70.	Introduction to IoT	18EC4109	Employability
71.	Adaptive Signal Processing	18EC4013	Employability
72.	Cognitive Radio	18EC4014	Employability
73.	Image & Video Processing	18EC4015	Employability
74.	Pattern Recognition and Machine learning	18EC4016	Employability
75.	Detection & Estimation of Signals	18EC4017	Employability
76.	Advanced Communications Lab (Virtual Lab)	18EC4018	Skill Development
77.	Image & Video Processing Lab	18EC4019	Skill Development
78.	Mini Project	18EC4020	Skill Development
79.	Constitution of India	18HS0829	Skill Development
80.	Pedagogy Studies	18HS0827	Skill Development
81.	Stress Management by Yoga	18HS0828	Skill Development
82.	Personality Development Through Life Enlightenment Skills	18HS0819	Skill Development
83.	Scripting Language for VLSI Design Automation	18EC4218	Employability
84.	Nano Materials and Nanotechnology	18EC4219	Employability
85.	Wireless Sensor Networks	18EC4008	Employability
86.	Business Analytics	18HS0824	Entrepreneurship
87.	Industrial Safety	18ME3121	Employability

88.	Advanced Operations Research	18ME3122	Employability
89.	Cost Management of Engineering Projects	18CE1028	Employability
90.	Composite Materials	18ME3123	Skill Development
91.	Waste to Energy	18EE2128	Skill Development
92.	Dissertation Phase I	18EC4220	Employability
93.	Dissertation Phase II	18EC4221	Employability
94.	VLSI Technology	18EC4201	Employability
95.	Digital IC Design	18EC4202	Employability
96.	ASIC Design	18EC4203	Employability
97.	System Modeling & Simulation	18EC4204	Employability
98.	Embedded System Design	18EC4101	Employability
99.	Verilog HDL	18EC4205	Employability
100.	Analog IC Design	18EC4206	Employability
101.	Image & Video Processing	18EC4015	Employability
102.	Digital Electronic Circuits Lab (Virtual Lab)	18EC4207	Skill Development
103.	Digital IC Design Lab	18EC4208	Skill Development
104.	Research Methodology and IPR	18HS0823	Skill Development
105.	English for Research Paper Writing	18HS0818	Skill Development
106.	Disaster Management	18CE1029	Skill Development
107.	Sanskrit for Technical Knowledge	18HS0825	Skill Development
108.	Value Education	18HS0826	Skill Development
109.	FPGA Architectures & Applications	18EC4209	Employability
110.	Low Power VLSI Design	18EC4210	Employability
111.	Nano Electronics	18EC4211	Employability
112.	Algorithms for VLSI Design Automation	18EC4212	Employability
113.	Advanced Digital System Design	18EC4001	Employability
114.	Testing & Testability	18EC4213	Employability
115.	Real Time Operating System	18EC4104	Employability
116.	Solid State Device Modeling and Simulation	18EC4214	Employability
117.	Mixed Signal Lab	18EC4215	Employability
118.	Digital VLSI Design Lab (Virtual Lab)	18EC4216	Employability
119.	Mini Project	18EC4217	Employability
120.	Constitution of India	18HS0829	Skill Development

121.	Pedagogy Studies	18HS0827	Skill Development
122.	Stress Management by Yoga	18HS0828	Skill Development
123.	Personality Development Through Life Enlightenment Skills	18HS0819	Skill Development

Modifications described above are carried out to the curriculum after discussions in the BoS by considering the feedback/suggestions from the stakeholders viz. students, alumni, faculty and employers.

**Agenda: 3**

Approval of syllabus for PG in ECE w.e.f., A.Y.2018-19

**Resolution: 3**

After the thorough discussion syllabus was framed to make the students acquire the required technical knowledge and skills. The BOS resolved to approve the syllabi framed for the PG (given in **Annexure-III**)

**Agenda: 4**

Approval of syllabus for the subjects offered to various branches w.e.f. 2018-19

**Resolution: 4**

After the thorough discussion syllabus was framed to make the students acquire the required technical knowledge and skills. The BOS resolved to approve the syllabi framed for the subjects offered to various branches (given in **Annexure-IV**).

**Agenda: 5**

Approval of Panel of Question Paper setters

**Resolution: 5**

Approved the panel of question paper setting (given in **Annexure –V**) to be submitted to the college academic council for approval.

**Agenda: 6**

Approval of Panel of Examiners

**Resolution: 6**

Approved the panel of examiners prepared for valuation (given in **Annexure –VI**) to be submitted to the college academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BOS. The meeting was concluded with vote of Thanks proposed by the Chairman-BOS.

**Agenda: 7**

Procedures of Evaluation.

**Resolution: 7**

The internal examination answer sheets are evaluated firmly according to the scheme of evaluation. Unit tests and assignments should help us to analyze the understanding of concepts by the students.

**Agenda: 8**






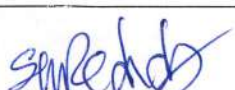


Academic facilities improvement

**Resolution: 8**

Looking at the facilities in the department committee suggested to improve the facilities to implement ICT in the classrooms and also add additional lab facilities that will helpful to the students to perform additional experiments.

### 3<sup>rd</sup> BOS Meeting on 18/06/2018

#### Members Present

S.No.	Member Name	Academic/ Industry Position	Role in the BOS	Signature
1.	Dr. M.Janardhana Raju	Professor& HOD-SIETK	Chairman	
2.	Dr.P.Rathnakamala	Professor-SIETK (Wireless Communication)	Member	
3.	Mr. V.Viswanadha	Associate Professor-SIETK (DECS)	Member	
4.	Mr. C. Vijaya Bhaskar	Associate Professor-SIETK (VLSI )	Member	
5.	Mrs.J.Jhansi	Associate Professor-SIETK (Embedded Systems)	Member	
6.	Dr. S. Narayana Reddy	Professor, S.V. University, Tirupati	Member	
7.	Dr. Rama Komaragiri	Associate Professor, NIT Calicut	Member	- Absent -
8.	Dr.P.Ramana Reddy	Professor JNTUA,Ananthapuramu	Member	 18/06/18
9.	Mr. Narendra Reddy	Scientist-C, CMTI, Bangalore	Member	
10.	Mr. B.Venkatadri	Software Developer HCL Technologies	Member	- Absent -