



SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR
(Autonomous)

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)
(Accredited by NBA & Accredited by NAAC with 'A' Grade)
(An ISO 9001:2008 Certified Institution)
Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583

Examinations Branch


B.Tech II Yecr I Semester (R18) Regular Examinations Nov-Dec-2019

Substitute Subjects Time Table

Time: 10:00 AM to 01:00 PM

BRANCH	COMPUTER SCIENCE AND ENGINEERING	ELCTRONICS & COMMUNICATION ENGINEERING
25-11-2019 Monday	1BCSO502 Digital Logic Design	---
04-12-2019 Wednesday	---	18CE0101 Engineering Mechanics

- Note: 1. If any omissions or clashes in this time table may please be informed to the undersigned immediately.
2. If any discrepancies are found the same may be informed to the undersigned immediately.
3. Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual


CONTROLLER OF EXAMINATIONS


PRINCIPAL
Siddharth Institute of Engineering & Technology
Siddharth Nagar,
PUTTUR-517583, Chittoor (Dt).