SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR
(Autonomous)
(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)
(Accredited by NBA & Accredited by NAAC with 'A' Grade)
(An ISO 9001:2008 Certified Institution)
Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583

Examinations Branch

TIME TABLE

B.Tech I Year II Semester (R18) Supplementary Examinations Dec-2019

Time: 02:00 PM to 05:00 PM

BRANCH	CE	AGE	ME	EEE	ECE	CSE	CSIT
09-12-2019 Monday	18HS0831 Mathematics – II (Common to All)						
11-12-2019 Wednesday	18HS0801 Chemistry (Common to CE, AGE, ME & EEE)				18HS0851 Semiconductor Physics (Common to CSE, CSIT & ECE)		
13-12-2019 Friday	18HS0810 English (Common to CE, AGE, ME & EEE)				18CS0501 Programming for Problem Solving (Common to CSE, CSIT & ECE)		
16-12-2019 Monday	18ME0302 Engineering Graphics & Design (Common to CE, AGE, ME & EEE)				18CE0101 Engineering Mechanics	18CS0502 Digital Logic Design (Common to CSE & CSIT)	
18-12-2019 Wednesday	18EE0240 Basic Electrical and Electronics Engineering (Common to CE & AGE)		18ME0303 Materials Engineering	18EE0201 Electrical Circuits - I	18EE0239 Basic Electrical Engineering (Common to CSE, CSIT & ECE)		

Note: 1. If any omissions or clashes in this time table may please be informed to the undersigned immediately.
2. If any discrepancies are found the same may be informed to the undersigned immediately.
3. Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual

CONTROLLER OF EXAMINATIONS

PRINCIPAL