



SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR

(Autonomous)

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuram)

(Accredited by NBA & Accredited by NAAC with 'A' Grade)

(An ISO 9001:2008 Certified Institution)

Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583

Examinations Branch


TIME TABLE

B.Tech III Year II Semester (R16) Supplementary Examinations Dec-2019

Time: 10:00 AM to 01:00 PM

BRANCH	CE	EEE	ECE	CSE	ME
10-12-2019 Tuesday	16CE125 Structural Analysis -II	16EE223 Power Semiconductor Drives	16EC421 Digital Communications	16CS524 Compiler Design	16HS605 Environmental Studies
12-12-2019 Thursday	16CE126 Design & Drawing of Steel Structures	16EE224 Electrical and Electronic Measurements	16EC422 Digital Signal Processing	16CS525 Software Testing	16MB750 Managerial Economics & Financial Analysis
17-12-2019 Tuesday	16CE127 Geotechnical Engineering - II	16EC423 Microprocessors & Microcontrollers (Common to EEE, ECE & CSE)			16ME319 Design of Machine Elements - II
19-12-2019 Thursday	16CE128 Water Resources Engineering - II	16EE226 Power System Analysis	16EC424 Digital IC Applications	16CS526 Object Oriented Analysis and Design	16ME320 Heat Transfer
21-12-2019 Saturday	16CE129 Transportation Engineering - I	16EE225 Switch Gear and Protection	16EC425 Microwave Engineering	16CS527 Computer Networks	16ME321 CAD/CAM

- Note: 1. If any omissions or clashes in this time table may please be informed to the undersigned immediately.
2. If any discrepancies are found the same may be informed to the undersigned immediately.
3. Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual.


CONTROLLER OF EXAMINATIONS


PRINCIPAL