



SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR
(Autonomous)

Examinations Branch

I YEAR M.Tech – II Semester (R16) Regular Examinations May/June 2017

Time: 10:00 AM to 1:00 PM

Date / Day	Control Systems (CS)	Power Electronics (PE)	Embedded Systems (ES)	VLSI (VLSI)	Digital Electronics And Communication Systems (DECS)
29-05-2017 Monday	PROCESS DYNAMIC AND CONTROL 16EE7508	POWER ELECTRONIC CONTROL OF AC DRIVES 16EE4307	REAL TIME OPERATING SYSTEMS 16EC5506 (Common to ES & VLSI)		MICRO COMPUTER SYSTEM DESIGN 16EC3807
31-05-2017 Wednesday	NON-LINEAR CONTROL THEORY 16EE7509	ADVANCED POWER SEMICONDUCTOR DEVICES & PROTECTION 16EE4308	TESTING & TESTABILITY 16EC5507 (Common to ES & VLSI)		IMAGE & VIDEO PROCESSING 16EC3808
02-06-2017 Friday	OPTIMAL CONTROL THEORY 16EE7510	FLEXIBLE AC TRANSMISSION SYSTEMS 16EE4309	HARDWARE SOFTWARE CO-DESIGN 16EC5508	LOW POWER VLSI DESIGN 16EC5709	WIRELESS COMMUNICATIONS 16EC3809
05-06-2017 Monday	ADVANCED DIGITAL SIGNAL PROCESSING 16EE7511	MODERN POWER ELECTRONICS 16EE4310	CRYPTOGRAPHY & NETWORK SECURITY 16EC5509	ALGORITHMS FOR VLSI DESIGN AUTOMATION 16EC5710	CODING THEORY & TECHNIQUES 16EC3810
07-06-2017 Wednesday	ELECTIVE-III ROBUST CONTROL 16EE7513	HVDC TRANSMISSION 16EE4311	FPGA ARCHITECTURE AND APPLICATIONS 16EC5708 (Common to ES & VLSI)		DETECTION & ESTIMATION OF SIGNALS 16EC3811
09-06-2017 Friday	ELECTIVE-IV POWER PLANT INSTRUMENTATION 16EE7514	ELECTIVE-II ENERGY AUDITING CONVERSATION AND MANAGEMENT 16EE4313	ELECTIVE-II RADIO FREQUENCY IDENTIFICATION 16EC5510	SCRIPTING LANGUAGE FOR VLSI DESIGN AUTOMATION 16EC5711	ELECTIVE-II OPTICAL NETWORKS 16EC3813



SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR
(Autonomous)

Examinations Branch

I YEAR M.Tech – II Semester (R16) Regular Examinations May/June 2017

Time: 10:00 AM to 1:00 PM

Date / Day	Structural Engineering (Struc. Engg)	Thermal Engineering (Ther. Engg.)	Computer Science & Engineering (CSE)
29-05-2017 Monday	ADVANCED REINFORCED CONCRETE DESIGN 16CE2010	ENERGY MANAGEMENT 16ME8810	SOFTWARE ARCHITECTURE AND DESIGN PATTERNS 16CS5810
31-05-2017 Wednesday	ADVANCED STRUCTURAL STEEL DESIGN 16CE2011	ADVANCED I.C. ENGINES 16ME8811	CLOUD COMPUTING 16CS5811
02-06-2017 Friday	FINITE ELEMENT METHODS 16CE2012	COMPUTATIONAL FLUID DYNAMICS 16ME8812	DATA ANALYTICS 16CS5812
05-06-2017 Monday	THEORY AND DESIGN OF PLATES AND SHELLS 16CE2013	THERMAL MEASUREMENTS AND PROCESS CONTROLS 16ME8813	JAVA & WEB TECHNOLOGIES 16CS5813
07-06-2017 Wednesday	STABILITY OF STRUCTURES 16CE2014	ALTERNATIVE ENERGY SOURCES 16ME8814	OBJECT ORIENTED ANALYSIS AND DESIGN 16CS5814
09-06-2017 Friday	ELECTIVE-II CONSTRUCTION PROJECT MANAGEMENT 16CE2016	ELECTIVE-II JET PROPULSION & ROCKETRY 16ME8817	ELECTIVE-II DISTRIBUTED SYSTEMS 16CS5816

- Note: 1. If any omissions or clashes in this time table may please be informed to the undersigned immediately.
2. If any discrepancies are found the same may be informed to the undersigned immediately.
3. Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual.

S. Sridhar
27/4/17
CONTROLLER OF EXAMINATIONS
Controller of Examinations
Siddharth Institute of Engineering & Technology
(AUTONOMOUS)
PUTTUR-517 583, Chittoor (DIST) A.P.

K. Sreedhar
PRINCIPAL
PRINCIPAL
Siddharth Institute of Engineering & Technology
Siddharth Nagar
PUTTUR - 517 583, Chittoor (Dt.)