



**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR
(AUTONOMOUS)**

Examination Branch

B.Tech II Year I Semester (R16) Regular Examinations Nov/Dec – 2017

Time: 10:00 AM to 1:00 PM

Date/ Day	Civil Engineering (CE)	Electrical & Electronics Engineering (EEE)	Electronics & Communication Engineering (ECE)	Computer Science & Engineering (CSE)	Mechanical Engineering (ME)
20-11-2017 Monday	Engineering Mathematics-III 16HS612 (Common to All)				
22-11-2017 Wednesday	Electrical & Mechanical Technology 16EE609	Environmental Studies 16HS605 (Common to EEE,ECE,CSE)			Material Sciences and Metallurgy 16ME303
24-11-2017 Friday	Strength of Materials -I 16CE103	Network Analysis Synthesis 16EE203	Switching Theory & Logic Design 16EC402	Advanced Data Structures through C++ 16CS505	Kinematics of Machinery 16ME304
27-11-2017 Monday	Surveying 16CE105	Basic Electronic Devices 16EC401	Basic Electronic Devices 16EC401	Digital Logic Design 16CS506	Machine Drawing 16ME305
29-11-2017 Wednesday	Fluid Mechanics 16CE106	Generation of Electric Power 16EE210	Signal and Systems 16EC403	Mathematical Foundations of Computer Science 16CS507	Strength of Materials 16CE104
01-12-2017 Friday	Building Materials & Construction 16CE107	Electrical Machines-I 16EE211	Random Signal & Stochastic Processes 16EC404	Basic Electrical and Electronics Engineering 16EE207	Fluid Mechanics & Hydraulic Machinery 16CE112

- Note:** 1. If any omissions or clashes in this time table may please be informed to the undersigned immediately.
2. If any discrepancies are found the same may be informed to the undersigned immediately.
3. Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual.

B. Sridhar
CONTROLLER OF EXAMINATIONS

Notification Date: 25-10-2017

Controller of Examinations

Siddharth Institute of Engineering & Technology
(AUTONOMOUS)

PUTTUR-517 583, Chittoor (DIST) A.P.

K. Sreeddy
PRINCIPAL

PRINCIPAL

Siddharth Institute of Engineering & Technology

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