



SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR
(Autonomous)

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)
(Accredited by NBA & Accredited by NAAC with 'A' Grade)
(An ISO 9001:2008 Certified Institution)
Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583


Examination Branch


B.Tech II Year II Semester (R16) Supplementary Examinations December-2018

Time:10:00 AM to 01:00 PM

BRANCH	CE	EEE	ME	CSE	ECE
11-12-2018 Tuesday	16HS613 Probability & Statistics (Common to CE, EEE, ME & CSE)				16EC407 Electronic Circuit Analysis
13-12-2018 Thursday	16HS605 Environmental Studies	16CE112 Fluid Mechanics & Hydraulic Machinery	16EE207 Basic Electrical & Electronics Engineering	16CS509 Object Oriented Programming	16EC408 Computer Organization and Architecture
15-12-2018 Saturday	16CE110 Building Planning & Drawing	16EE214 Electromagnetic Fields	16ME306 Manufacturing Technology	16CS510 Computer Organization	16EC409 Electromagnetic Theory and Transmission Lines
18-12-2018 Tuesday	16CE111 Strength of Materials - II	16EC411 Analog Electronic Circuits	16ME307 Engineering Thermodynamics	16CS511 Database Management Systems	16EC410 Pulse & Digital Circuits
20-12-2018 Thursday	16CE113 Hydraulics & Hydraulic Machinery	16EE215 Electrical Machines -II	16ME308 Dynamics of Machinery	16CS512 Operating Systems	16EE212 Electrical Technology

- Note:1. If any omissions or clashes in this time table may please be informed to the undersigned immediately.
2. If any discrepancies are found the same may be informed to the undersigned immediately.
3. Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual.


CONTROLLER OF EXAMINATIONS
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