



SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR
(Autonomous)

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)
(Accredited by NBA & Accredited by NAAC with 'A' Grade)
(An ISO 9001:2008 Certified Institution)
Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583

Examinations Branch

TIME TABLE

M.Tech I Year I Semester (R19) Regular Examinations - Jan-2020

Time: 10.00 AM to 01.00 PM

Date/Day	STRUCTURAL ENGINEERING	THERMAL ENGINEERING	COMPUTER SCIENCE AND ENGINEERING	POWER ELECTRONICS	VLSI	EMBEDDED SYSTEMS
20-01-2020 Monday			19HS0823 Research Methodology and IPR (Common to All)			
22-01-2020 Wednesday	19CE1001 Advanced Structural Analysis	19ME3101 Thermodynamics and Combustion	19HS0841 Discrete Mathematics and Applications	19EE2101 Electric Drives Systems	19EC4101 Embedded System Design (Common to VLSI & ES)	
24-01-2020 Friday	19CE1002 Advanced Solid Mechanics	19ME3102 Advanced Fluid Dynamics	19CS5001 Advanced Data Structures	19EE2102 Modeling and Analysis of Electrical Machines	19EC4201 VLSI Technology	19EC4102 Sensors and Actuators
27-01-2020 Monday	19CE1010 Theory of Structural Stability	19ME3112 Nuclear Engineering	19CS5010 Machine Learning	19EE2103 Advanced Power Electronic Circuits	19EC4202 Digital IC Design	19EC4104 Real Time Operating Systems
29-01-2020 Wednesday	19CE1011 Structural Health Monitoring	19ME3114 Air Conditioning System Design	19CS5014 Distributed Systems	19EE2107 Static VAR Controllers and Harmonic Filtering	19EC4205 Verilog HDL	19EC4011 Wireless Communications

- Note: 1. If any omissions or clashes in this time table may please be informed to the undersigned immediately.
2. If any discrepancies are found the same may be informed to the undersigned immediately.
3. Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual.


CONTROLLER OF EXAMINATIONS


PRINCIPAL