



SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR

(Autonomous)

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)

(Accredited by NBA & Accredited by NAAC with 'A' Grade)

(An ISO 9001:2008 Certified Institution)

Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583

Examinations Branch

I M.Tech II Semester (R20) Regular Examinations November-2021

LAB EXAMINATION SCHEDULE

Date/Day	STRUCTURAL ENGINEERING	VLSI	THERMAL ENGINEERING
15-11-2021 (MONDAY)	20CE1007 Structural Dynamics lab (Virtual Lab)	20EC4215 Mixed Signal Lab	20ME3107 Computational Fluid Dynamics Lab
16-11-2021 (TUESDAY)	20HS0838 Numerical Analysis Lab	20EC4216 Digital VLSI Design Lab (Virtual Lab)	20ME3108 Thermal Engineering Lab (Virtual Lab)
17-11-2021 (WEDNESDAY)	20CE1025 Mini Project	20EC4217 Mini Project	20ME3109 Mini-Project


CONTROLLER OF EXAMINATIONS

Controller of Examinations
Siddharth Institute of Engineering & Technology
(AUTONOMOUS)
PUTTUR - 517 583, Chittoor (DIST.) A.P


PRINCIPAL

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Siddharth Institute of Engineering & Technology
Siddharth Nagar,
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