

SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR
FACULTY PROFILE

Personal Details

Name : Dr. S. Kannappan
Date of Birth : 31-05-1987
Address : 5-57, Sathrawada, Nagari Mandal,
Chittoor District



Date of Joining in this institute : 01-07-2023

Academic Details

| Qualification | Specialization | Name of the Board/University | Year of Passing |
|---------------|----------------|----------------------------------------------------------|-----------------|
| Ph.D. | VLSI | Jawaharlal Nehru Technological University, Ananthapuramu | 2020 |
| M.Tech. | DECS | JNTUA College of Engineering, Anantahapuramu | 2011 |
| B.Tech. | ECE | Jawaharlal Nehru Technological University, Ananthapuramu | 2008 |

Professional Experience

| Designation | Name of the Employer | Length of Service |
|---------------------|---------------------------------------------------------|-------------------------|
| Associate Professor | Siddharth Institute of Engineering & Technology, Puttur | 01-07-2023 to Till date |
| Associate Professor | GATES Institute of Technology, Gooty | 6Y |
| Assistant Professor | SITAMS, Chittoor | 4Y |
| Assistant Professor | Intell Engineering College, Anantapur | 2Y |

Professional Memberships

| | |
|---------|--|
| ➤ IEI | |
| ➤ ISTE | |
| ➤ IAENG | |

Research Publications

| National Journals | International Journals | National Conferences | International Conferences | Books / Book Chapters | Patents |
|-------------------|------------------------|----------------------|---------------------------|-----------------------|---------|
| 0 | 12 | 3 | 7 | 0 | 2 |

Seminars/Workshops/FDPs/NPTEL attended

| Seminars | Workshops | FDPs | NPTEL |
|----------|-----------|------|-------|
| 2 | 3 | 9 | 2 |

Other Achievements

- Received Indian Patent Grant with patent number 518011 for an invention titled "A Novel Architecture for MAC" in the year 2024.
- Qualified in UGC NET for Assistant Professor in the subject of Electronic Science in December 2023.