

**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR**  
**FACULTY PROFILE**

**Personal Details**

Name : D S VANAJA  
Date of Birth : 15-06-1991  
Address : 21-86A,DASARIGUNTA STREET,GATE  
PUTTUR,PUTTUR-517583,CHITTOOR DIST,A.P.



Date of Joining in this institute : 02-07-2024

**Academic Details**

Qualification	Specialization	Name of the Board/University	Year of Passing
M.Tech.	VLSI DESIGN	Sri venkatesa Perumal college of Engineering and technology, Puttur, JNTUA	2015
B.Tech.	ECE	Sri venkatesa Perumal college of Engineering and technology, Puttur, JNTUA	2012

**Professional Experience**

Designation	Name of the Employer	Length of Service
Assistant Professor	Siddharth Institute of Engineering & Technology, Puttur.	02-07-2024 to Till date
Assistant Professor	Sri Venkatesa Perumal college of Engineering and Technology, Puttur, JNTUA	7Y.10M

**Professional Memberships**

- ISTE
- IAENG

**Research Publications**

National Journals	International Journals	National Conferences	International Conferences	Books / Book Chapters	Patents
03	2	2	1	0	2

**Seminars/Workshops/FDPs/NPTEL attended**

Seminars	Workshops	FDPs	NPTEL
5	4	10	4

**Other Achievements**

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