

**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR**  
**FACULTY PROFILE**

**Personal Details**

Name : BrahminiPolamreddy  
Date of Birth : 03-06-1989  
Address : Narayanavanam  
Puttur



Date of Joining in this institute : 05-07-2022

**Academic Details**

| Qualification | Specialization     | Name of the Board/University | Year of Passing |
|---------------|--------------------|------------------------------|-----------------|
| M.Tech.       | VLSI System Design | JNTUA, Ananthapuramu         | 2015            |
| B.Tech.       | ECE                | JNTUA, Ananthapuramu         | 2011            |

**Professional Experience**

| Designation         | Name of the Employer                                    | Length of Service       |
|---------------------|---|-------------------------|
| Assistant Professor | Siddharth Institute of Engineering & Technology, Puttur | 05-07-2022 to Till date |

**Professional Memberships**

|         |  |
|---------|--|
| ➤ IAENG |  |
| ➤ ISTE  |  |

**Research Publications**

| National Journals | International Journals | National Conferences | International Conferences | Books / Book Chapters | Patents |
|-------------------|------------------------|----------------------|---------------------------|-----------------------|---------|
| -                 | 2                      | -                    | 1                         | -                     | -       |

**Seminars/Workshops/FDPs/NPTEL attended**

| Seminars | Workshops | FDPs | NPTEL |
|----------|-----------|------|-------|
| -        | -         | 2    | -     |

**Other Achievements**

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